Plastic-Encapsulate Darlington Transistors

ULN2402 Darlington Transistor (NPN)

1 Introduction

ULN2402 is a high voltage, high current Darlington transistor array. Each device consists of four NPN Darlington pairs that can be output independently. The collector current of a single Darlington pair is rated at 500mA, and parallel Darlington pairs provide a higher current. Applications include relay drivers, lamp drivers, display drivers, line drivers, and logic buffers.

Each Darlington pair of ULN2402 devices has a 2.7kΩ series base resistance that works directly with TTL or CMOS devices. This device is often used to drive a variety of loads, such as DC engine, LED display light, high power cache and general logic circuits such as TTL, 5V CMOS, etc.

In addition, each Darlington pair input stage of the ULN2402 is designed with a 4kΩ pull-down resistor to ground, which can prevent load misoperation caused by the uncertain state of the single-chip microcomputer.

3 Features

- 500mA rated collector current (single output)
- High voltage output: 40V
- Output clamp diode
- Compatible with all kinds of logic input
- 4kΩ pull-down resistor

4 Applications

- Relay Drivers
- Lamp Drivers
- Line Drivers
- Logic Buffers
- Stepper Motors
- IP Camera
- HVAC Valve and LED Dot Matrix

2 Available Package

PART NUMBER	PACKAGE
ULN2402-PBN	SOP10

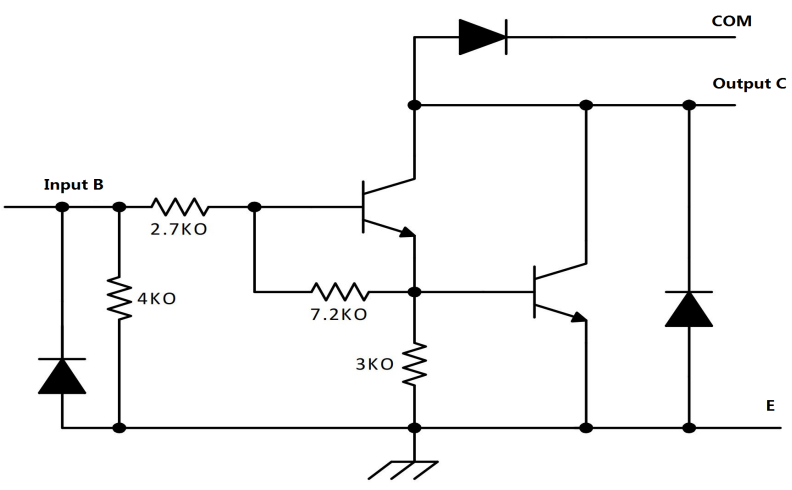


Figure 2-1. Functional Block Diagram

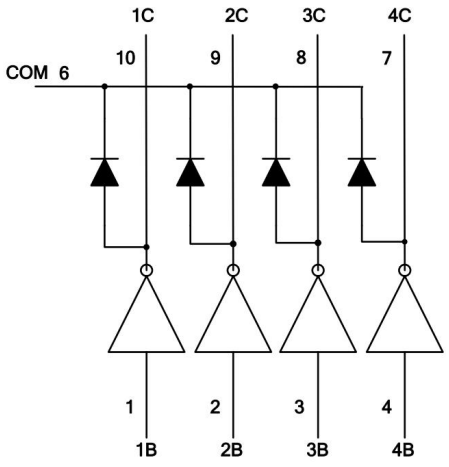


Figure 2-2. Simplified Block Diagram

5 Pin Configuration and Marking Information

5.1 Pin Configuration and Function

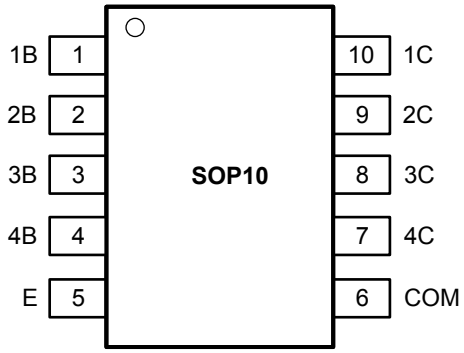
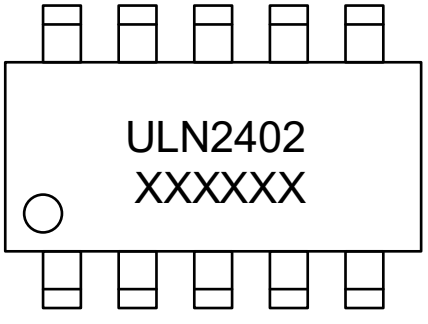


Figure 5-1. Pin Configuration (Top View)

PIN NAME	ULN2402-PBN	I / O	DESCRIPTION
	SOP10		
1B	1	I	Channel 1 through 4 Darlington base input
2B	2	I	
3B	3	I	
4B	4	I	
E	5	-	Common emitter shared by all channels (typically tied to ground)
COM	6	-	Common cathode node for flyback diodes (required for inductive loads)
4C	7	O	Channel 1 through 4 Darlington collector output
3C	8	O	
2C	9	O	
1C	10	O	

5.2 Marking Information



SOP10

Note:

"ULN2402" = Device Number.

"XXXXXX" = Code, Indicates weekly record information of production.

6 Specifications

6.1 Absolute Maximum Ratings

at 25°C free-air temperature (unless otherwise specified)(1)

CHARACTERISTIC			SYMBOL	VALUE	UNIT
Collector-emitter voltage(2)			V_{CE}	40	V
Clamp diodes reverse voltage(2)			V_{COM}	40	V
Input voltage(2)			V_{IN}	30	V
Peak collector current			I_{CP}	500	mA
Output clamp current			I_{OK}	500	mA
Total emitter-terminal current			I_{TE}	-1.4	A
Maximum power dissipation	ULN2402	SOP10	$P_{D\ MAX}$	Internally Limited(3)	W
Maximum junction temperature			$T_{J\ MAX}$	150	°C
Storage temperature			T_{stg}	-65 ~ 150	°C

(1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions*. Exposure to absolute-maximum rated conditions for extended periods may affect device reliability.

(2) All voltages are with respect to network ground terminal.

(3) Refer to *Thermal Information* for details.

6.2 Recommended Operating Conditions

PARAMETER	SYMBOL	MIN.	NOM.	MAX.	UNIT
Operating junction temperature	T_J	-20	-	125	°C
Operating ambient temperature	T_A	-20	-	85	°C

6.3 ESD Ratings

ESD RATINGS		SYMBOL	VALUE	UNIT
Electrostatic discharge(4)	Human body model	$V_{ESD-HBM}$	2000	V

(4) ESD testing is conducted in accordance with the relevant specifications formulated by the Joint Electronic Equipment Engineering Commission (JEDEC). The human body model (HBM) electrostatic discharge test is based on the JESD22-114D test standard, using a 100pF capacitor and discharging to each pin of the device through a resistance of 1.5kΩ.

6.4 Thermal Information

THERMAL METRIC(5)		SYMBOL	SOP8	UNIT
Thermal resistance (Junction-to-ambient)		$R_{\Theta JA}$	125	°C/W
Reference maximum power dissipation for continuous operation	T_A=+25°C	$P_{D\ Ref}$	1	W
	T_A=+85°C		0.5	

(5) T_A = 25°C, measured on evaluation board with 1oz. copper traces of minimum pad size, all device outputs were active.

6 Specifications

6.5 Electrical Characteristics

Unless otherwise noted T_A = 25°C.

PARAMETER	SYMBOL	TEST FIGURE	TEST CONDITIONS		MIN.	TYP.(6)	MAX.	UNIT
Input voltage (ON)	$V_{IN\ (ON)}$	Figure 3	V_{CE} = 1.5V	I_C = 30mA	-	1.5	2	V
				I_C = 60mA	-	1.6	2	
				I_C = 120mA	-	1.7	2.1	
				I_C = 240mA	-	1.9	2.2	
				I_C = 350mA	-	2.2	2.4	
			I_I = 800μA (V_{CE} < 1.5V)	I_C = 30mA	-	2.3	2.7	
				I_C = 60mA	-	2.4	2.74	
				I_C = 120mA	-	2.5	2.8	
				I_C = 240mA	-	2.7	2.93	
				I_C = 350mA	-	2.9	3.08	
			I_I = 1mA (V_{CE} < 1.5V)	I_C = 30mA	-	2.7	3.12	
				I_C = 60mA	-	2.8	3.17	
				I_C = 120mA	-	2.9	3.24	
				I_C = 240mA	-	3.1	3.4	
				I_C = 350mA	-	3.3	3.58	
Collector-emitter saturation voltage	$V_{CE(SAT)}$	Figure 4	V_I = 2.4V (I_I > 800μA)	I_C = 30mA	-	0.7	-	V
				I_C = 60mA	-	0.8	-	
				I_C = 120mA	-	0.9	-	
				I_C = 240mA	-	1.2	-	
				I_C = 350mA	-	1.4	-	
Input current	I_I	Figure 2	I_C = 60mA	V_I = 12V	-	5.7	-	mA
				V_I = 6V	-	2.7	-	
				V_I = 4.5V	-	1.9	-	
				V_I = 2.4V	-	0.8	-	
Clamp forward voltage	V_F	Figure 6	I_F = 350mA		-	1.6	2.0	V
Collector cutoff current	I_{CEX}	Figure 1	V_{CE} = 40V, I_I = 0		-	-	50	μA
Collector withstand voltage	V_{CE}	Figure 1	V_{CE} = 40V, I_I = 0		40	-	-	V
Clamp diode reverse leakage current	I_R	Figure 5	V_R = 40V		-	-	50	μA
Clamping diode reverse voltage	V_R	Figure 5	V_R = 40V		40	-	-	V
Turn-on delay time	t_{PLH}	Figure 7	V_L = 12V，R_L = 45Ω		-	0.15	1	μs
Turn-off delay time	t_{PHL}	Figure 7	V_L = 12V，R_L = 45Ω		-	0.15	1	μs

Note:

(6) Typical numbers are at 25°C and represent the most likely norm.

7 Parameter Measurement Information

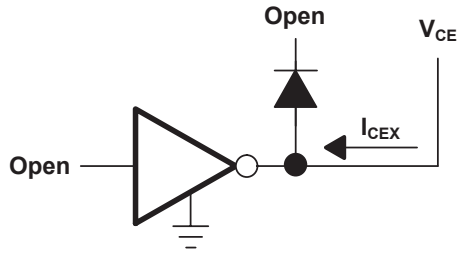


Figure 1. I_{CEX} Test Circuit

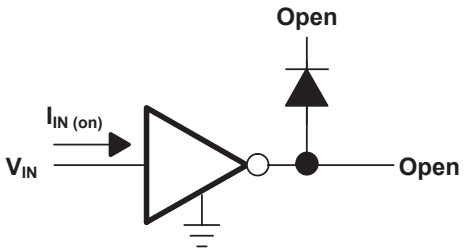


Figure 2. I_{IN} Test Circuit

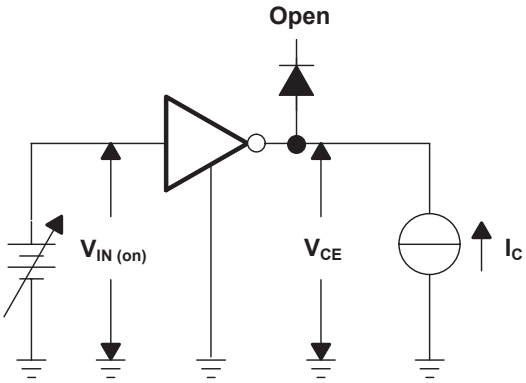


Figure 3. $V_{IN\ (on)}$ Test Circuit

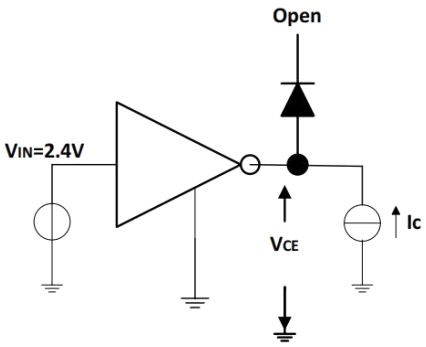


Figure 4. $V_{CE\ (SAT)}$ Test Circuit

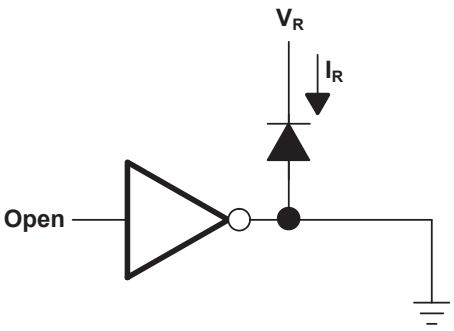


Figure 5. I_R Test Circuit

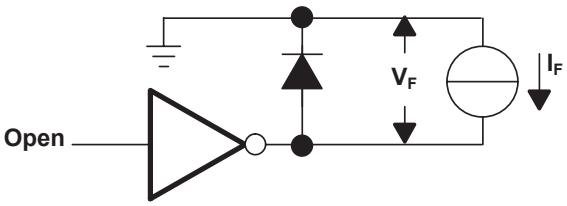
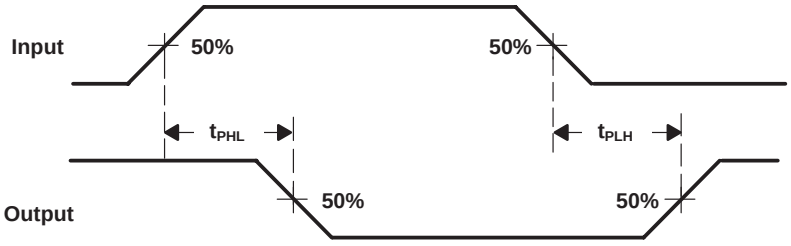


Figure 6. V_F Test Circuit

7 Parameter Measurement Information (continued)



VOLTAGE WAVEFORMS

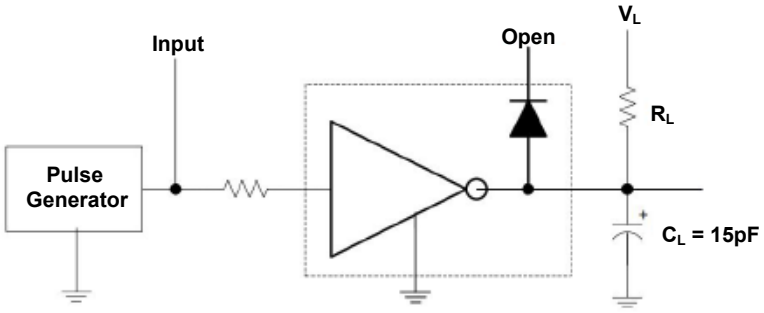


Figure 7. Propagation Delay-Time Waveforms

8 Typical Application

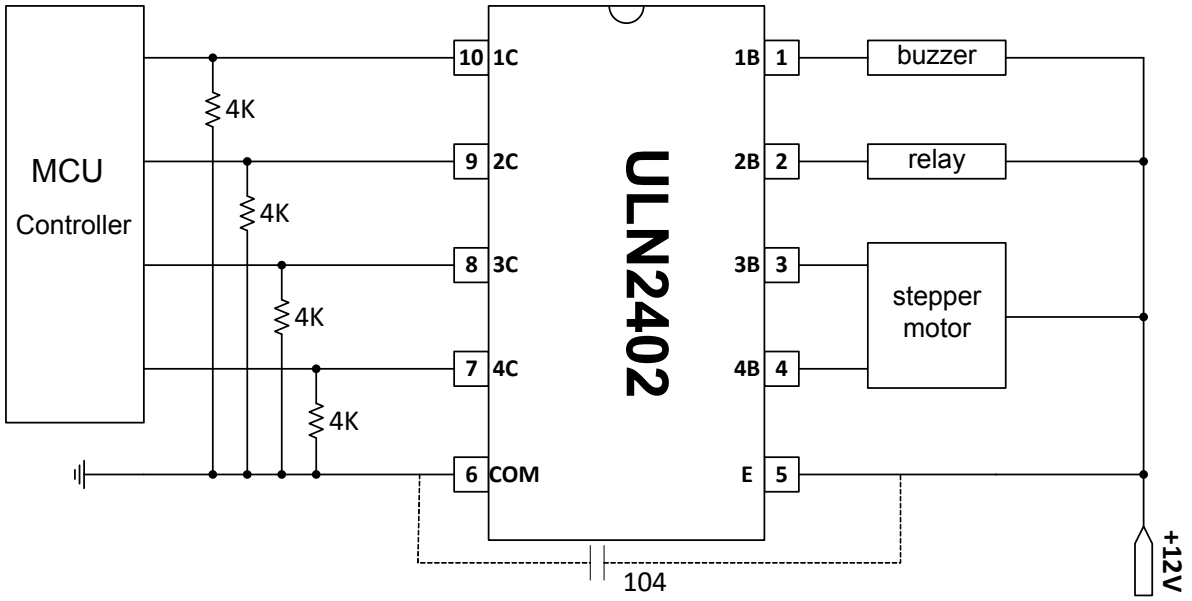


Figure 8-1. Typical application circuit

The applications of ULN2402 are not limited to the circuit diagram shown in Figure 8-1. Specifically, the driven load could be 4 relays, 4 LED diodes, or two outputs paralleled as one channel, depending on actual usage scenarios.

ULN2402 integrates 4kΩ pull-down resistors per channel, eliminating the need for external pull-down resistors.

When using an RC buck circuit to power ULN2402, note that RC circuits cannot suppress transient voltage spikes from the power grid. A 104 capacitor must be connected between the COM terminal and ground near ULN2402, as illustrated in Figure 8-1. This capacitor is unnecessary in other application scenarios.

NOTE

The application information in this section is not part of the data sheet component specification, and SZCJ makes no commitment or statement to guarantee its accuracy or completeness. Customers are responsible for determining the rationality of corresponding components in their circuit design and making tests and verifications to ensure the normal realization of their circuit design.

9 Notes and Revision History

9.1 Associated Product Family and Others

To view other products of the same type or IC products of other types, please click the official website of SZCJ -- ***https: www.szcjsemi.com*** for more details.

9.2 Notes

Electrostatic Discharge Caution



This IC may be damaged by ESD. Relevant personnel shall comply with correct installation and use specifications to avoid ESD damage to the IC. If appropriate measures are not taken to prevent ESD damage, the hazards caused by ESD include but are not limited to degradation of integrated circuit performance or complete damage of integrated circuit. For some precision integrated circuits, a very small parameter change may cause the whole device to be inconsistent with its published specifications.

9.3 Revision History

September, 2025: released ULN2402 rev - 1.0.

10 Orderable, Mechanical, and Packaging Information

The following pages include mechanical packaging and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser based versions of this data sheet, refer to the left hand navigation.

Orderable Information

MODEL	DEVICE	PACKAGE	OP TEMP	ECO PLAN	MSL	PACKING OPTION	SORT
ULN2402	ULN2402-PBN	SOP10	-40 ~ 85°C	RoHS & Green	Level 3 168 HR	Tape and Reel 4000 Units / Reel	Active
Others	-	-	-	-	-	-	Customized

Note:

ECO PLAN: For the RoHS and Green certification standards of this product, please refer to the official report provided by SZCJ .

MSL: Moisture Sensitivity Level. Determined according to JEDEC industry standard classification.

SORT: Specifically defined as follows:

Active: Recommended for new products;

Customized: Products manufactured to meet the specific needs of customers;

Preview: The device has been released and has not been fully mass produced. The sample may or may not be available;

NoRD: It is not recommended to use the device for new design. The device is only produced for the needs of existing customers;

Obsolete: The device has been discontinued.

DISCLAIMER

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