Itra Fast Response High PSRR Low Noise CMOS Voltage Regulators

CJ6216 Series Low-dropout Regulators

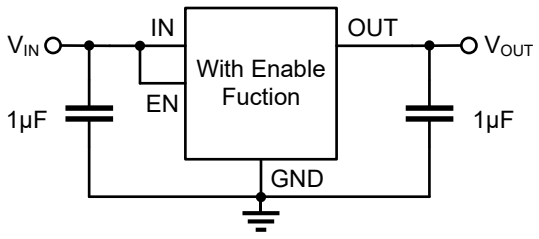
1 Introduction

The CJ6216 Series is a linear regulator capable of supplying 500mA output current. Designed to meet the requirements of RF and analog circuits, the CJ6216 Series provides low noise, high PSRR, low quiescent current and very good load / line transients. Using innovative design techniques,the CJ6216 series offers an ultra-low noise performance without the addition of a noise bypass capacitor. The CJ6216 Series can work with a 1μF input and 1μF output ceramic capacitor.

The CJ6216 Series is available in DFNWB1×1-4L, SOT-23-5L,and CSPC0606-4 packages.

2 Applications

- Smartphones/tablets
- Digital cameras
- Image sensors
- Instrumentation
- VCO and RF modules



Typical Application Circuits

3 Features

- Low output voltage noise: 4.5μVRMS
- Power Supply Rejection Ratio:
  98dB@1kHz (Typ.)
  91dB@10kHz (Typ.)
  73dB@100kHz (Typ.)
- Input Voltage Range: 1.8V ~ 5.5V
- Fixed Output Voltage:
  Available from 1.2V to 5.0V
- Output Voltage Tolerance: ±1%
- Output Current: up to 500mA
- Low Quiescent Current: 10μA (Typ.)
- Shutdown Current: 0.005μA (Typ.)
- Dropout Voltage:
  200mV@500mA
  (V_{OUT} = 2.8V,SOT-23-5L)
- Excellent Transient Response
- Integrated Fault Protection:
  - Inrush Current Limit
  - Output Current Limit
  - Thermal Shutdown
  - Short-Circuit Protection

4 Available Packages

PART NUMBER	PACKAGE
CJ6216 Series	SOT-23-5L
	DFNWB1×1-4L
	CSPC0606-4

5 Orderable Information

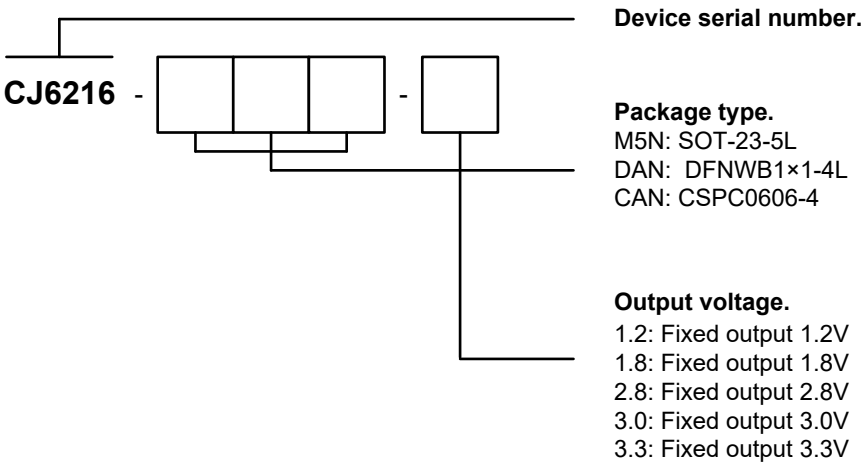


Figure 5-1. Naming Conventions

MODEL	DEVICE	PACKAGE	OP TEMP	ECO PLAN	MSL	PACKING OPTION	SORT
4 Pins Packaged Products							
CJ6216-1.2	CJ6216-DAN-1.2	DFNWB1×1-4L	-40 ~ 85°C	RoHS & Green	Level 3 168 HR	Tape and Reel 10000 Units / Reel	Active
CJ6216-1.8	CJ6216-DAN-1.8	DFNWB1×1-4L	-40 ~ 85°C	RoHS & Green	Level 3 168 HR	Tape and Reel 10000 Units / Reel	Active
CJ6216-2.8	CJ6216-DAN-2.8	DFNWB1×1-4L	-40 ~ 85°C	RoHS & Green	Level 3 168 HR	Tape and Reel 10000 Units / Reel	Active
CJ6216-2.9	CJ6216-DAN-2.9	DFNWB1×1-4L	-40 ~ 85°C	RoHS & Green	Level 3 168 HR	Tape and Reel 10000 Units / Reel	Active
CJ6216-3.0	CJ6216-DAN-3.0	DFNWB1×1-4L	-40 ~ 85°C	RoHS & Green	Level 3 168 HR	Tape and Reel 10000 Units / Reel	Active
CJ6216-3.3	CJ6216-DAN-3.3	DFNWB1×1-4L	-40 ~ 85°C	RoHS & Green	Level 3 168 HR	Tape and Reel 10000 Units / Reel	Active
CJ6216-1.8	CJ6216-CAN-1.8	CSP0.65×0.65-4L	-40 ~ 85°C	RoHS & Green	Level 3 168 HR	Tape and Reel 5000 Units / Reel	Active
CJ6216-2.8	CJ6216-CAN-2.8	CSP0.65×0.65-4L	-40 ~ 85°C	RoHS & Green	Level 3 168 HR	Tape and Reel 5000 Units / Reel	Active
CJ6216-3.0	CJ6216-CAN-3.0	CSP0.65×0.65-4L	-40 ~ 85°C	RoHS & Green	Level 3 168 HR	Tape and Reel 5000 Units / Reel	Active
CJ6216-3.3	CJ6216-CAN-3.3	CSP0.65×0.65-4L	-40 ~ 85°C	RoHS & Green	Level 3 168 HR	Tape and Reel 5000 Units / Reel	Active

5 Orderable Information

5 Pins Packaged Products							
CJ6216-1.8	CJ6216-M5N-1.8	SOT-23-5L	-40 ~ 125°C	RoHS & Green	Level 3 168 HR	Tape and Reel 3000 Units / Reel	Active
CJ6216-2.8	CJ6216-M5N-2.8	SOT-23-5L	-40 ~ 125°C	RoHS & Green	Level 3 168 HR	Tape and Reel 3000 Units / Reel	Active
CJ6216-3.0	CJ6216-M5N-3.0	SOT-23-5L	-40 ~ 125°C	RoHS & Green	Level 3 168 HR	Tape and Reel 3000 Units / Reel	Active
CJ6216-3.3	CJ6216-M5N-3.3	SOT-23-5L	-40 ~ 125°C	RoHS & Green	Level 3 168 HR	Tape and Reel 3000 Units / Reel	Active
MODEL	**DEVICE**	**PACKAGE**	**OP TEMP**	**ECO PLAN**	**MSL**	**PACKING OPTION**	**SORT**
Customized Products							
CJ6216-x.x	CJ6216-DAN-x.x	DFNWB1×1-4L	-40 ~ 125°C	RoHS & Green	Level 3 168 HR	Tape and Reel 10000 Units / Reel	Customized
	CJ6216-CAN-x.x	CSPC0606-4	-40 ~ 125°C	RoHS & Green	Level 3 168 HR	Tape and Reel 5000 Units / Reel	Customized
	CJ6216-M5N-x.x	SOT-23-5L	-40 ~ 125°C	RoHS & Green	Level 3 168 HR	Tape and Reel 3000 Units / Reel	Customized
Others	-	-	-	-	-	-	Customized

Note:

ECO PLAN: For the RoHS and Green certification standards of this product, please refer to the official report provided by SZCJ

MSL: Moisture Sensitivity Level. Determined according to JEDEC industry standard classification.

SORT: Specifically defined as follows:

Active: Recommended for new products;

Customized: Products manufactured to meet the specific needs of customers;

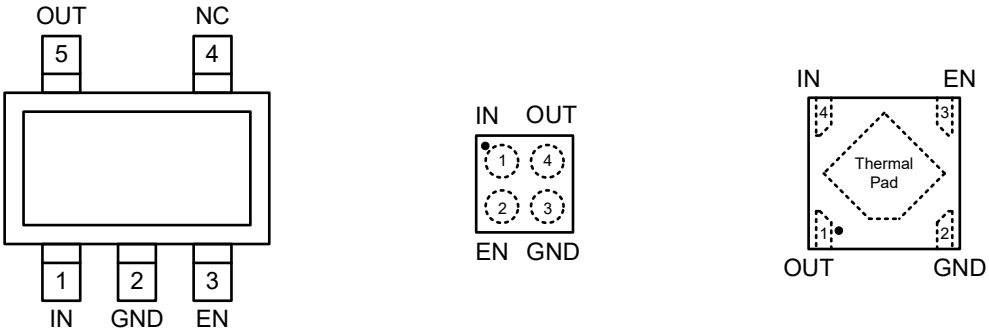
Preview: The device has been released and has not been fully mass produced. The sample may or may not be available;

NoRD: It is not recommended to use the device for new design. The device is only produced for the needs of existing customers;

Obsolete: The device has been discontinued.

6 Pin Configuration and Marking Information

6.1 Pin Configuration



CJ6216-M5N-x.x
SOT-23-5L

CJ6216-CAN-x.x
CSPC0606-4

CJ6216-DAN-x.x
DFNWB1×1-4L

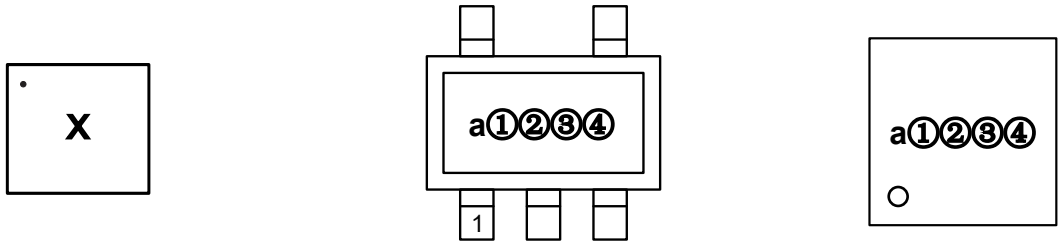
Figure 6-1. Package Top View (Not to Scale)

6.2 Pin Function

PIN NAME	I / O	CJ6216 Series Pin Function
		DESCRIPTION
IN	I	Input to the device. Use the recommended value of the input capacitor and place it as close to the input of the device as possible to reduce the impedance of the input supply.
GND	-	Regulator ground.
EN	I	Enable pin. Driving this pin to logic high enables the device, driving this pin to logic low disables the device. The EN pin can be floating, and when the EN is floating, it will be pulled down to ground internally. If enable functionality is not required, it's recommended to connect EN to IN.
NC	-	No internal connection. This pin can float, but when this pin is connected to GND, the device has better thermal performance.
OUT	O	Output of the regulator. An output capacitor is required for stability and help device obtain the best transient response. Use the capacitor with the recommended value and place it as close as possible to the output.
Thermal Pad	-	Connect the thermal pad to a large-area ground plane. The thermal pad is internally connected to GND.

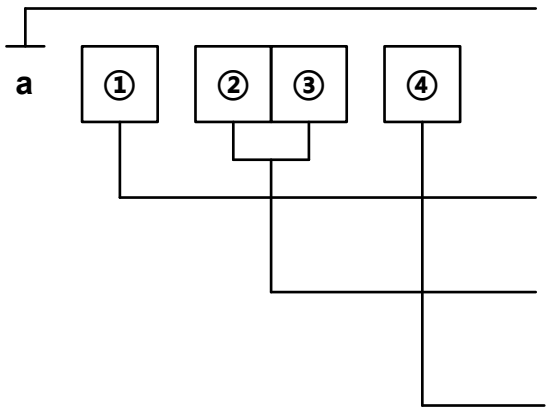
6 Pin Configuration and Marking Information

6.3 Marking Information



CSPC0606-4 **SOT-23-5L** **DFNWB1×1-4L**

"X": Specific Device Code.



Serial code for CJ6216 series.

Representative output voltage.
B: Fixed output 1.2V
D: Fixed output 1.8V
F: Fixed output 2.8V
G: Fixed output 3.0V
H: Fixed output 3.3V
(Other products are customized)

Code, indicates weekly record information of production.

Code, special pin arrangement sequence.
(blank): Normal

Figure 6-1. Marking Rule

Output Voltage	Marking Information for CJ6216 Series		
	4-Pins Packages		5-Pins Packages
	DFNWB1×1-4L	CSP0.65×0.65-4L	SOT-23-5L
1.8V	CJ6216-DAN-1.8: aDXX	CJ6216-CAN-1.8: B	CJ6216-M5N-1.8: aDXX
2.8V	CJ6216-DAN-2.8: aFXX	CJ6216-CAN-2.8: A	CJ6216-M5N-2.8: aFXX
2.9V	CJ6216-DAN-2.9: aZXX	-	CJ6216-M5N-2.9: aZXX
3.0V	CJ6216-DAN-3.0: aGXX	-	CJ6216-M5N-3.0: aGXX
3.3VV	CJ6216-DAN-3.3: aHXX	CJ6216-CAN-3.3: C	CJ6216-M5N-3.3: aHXX

7 Specifications

7.1 Absolute Maximum Ratings

(T_A = 25°C, unless otherwise specified)(1)

CHARACTERISTIC			SYMBOL	VALUE	UNIT
Input voltage range(2)			V_{IN}	-0.3 ~ 6.5	V
Enable input voltage range(2)			V_{EN}	VSS-0.3 ~ VIN+0.3	
Output voltage range(2)			V_{OUT}	VSS-0.3 ~ VIN+0.3	
Maximum power dissipation	CJ6216 series	DFNWB1×1-4L	$P_{D\ Max}$	Internally Limited(3)	W
		CSPC0606-4			
		SOT-23-5L			
Maximum junction temperature			$T_{J\ Max}$	150	°C
Storage temperature			T_{stg}	-40 ~ 150	°C
Soldering temperature & time			T_{solder}	260°C, 10s	-

(1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions*. Exposure to absolute-maximum rated conditions for extended periods may affect device reliability.

(2) All voltages are with respect to network ground terminal.

(3) Refer to *Thermal Information* for details.

7.2 Recommended Operating Conditions

PARAMETER	SYMBOL	MIN.	NOM.	MAX.	UNIT
Input voltage	V_{IN}	1.8	-	5.5	V
Operating junction temperature	T_J	-40	-	125	°C

7.3 ESD Ratings

ESD RATINGS		SYMBOL	VALUE	UNIT
Electrostatic discharge(4)	Human body model	$V_{ESD-HBM}$	2000	V

(4) ESD testing is conducted in accordance with the relevant specifications formulated by the Joint Electronic Equipment Engineering Commission (JEDEC). The human body model (HBM) electrostatic discharge test is based on the JS-001: 2017 test standard, using a 100pF capacitor and discharging to each pin of the device through a resistance of 1.5kΩ.

7 Specifications

7.4 Thermal Information

THERMAL METRIC(5)	SYMBOL	CJ6216 Series		UNIT
Junction-to-ambient thermal resistance	$R_{\Theta JA}$	**SOT-23-5L**	**DFNWB1×1-4L**	°C/W
		250.0	220.0	
		CSPC0606-4	**-**	
		-	-	
Junction-to-case thermal resistance	$R_{\Theta JC}$	**SOT-23-5L**	**DFNWB1×1-4L**	°C/W
		65.0	115.0	
		CSPC0606-4	**-**	
		-	-	
Reference maximum power dissipation for continuous operation	$P_{D\ Ref}$	**SOT-23-5L**	**DFNWB1×1-4L**	W
		0.40	0.45	
		CSPC0606-4	**-**	
		-	-	

(5) Thermal metric is measured in still air with T_A = 25°C and installed on a 1 in^2 FR-4 board covered with 2 ounces of copper.

7 Specifications

7.5 Electrical Characteristics

CJ6216 Series (V_{IN} = V_{OUT} +1V, C_{IN} = 1μF, C_{OUT} = 1μF, T_A = 25°C, unless otherwise specified)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS		MIN.	TYP.(6)	MAX.	UNIT
Input voltage range	V_{IN}			1.8	-	5.5	V
DC output tolerance	-	T_J = 25°C, I_{OUT} = 10mA		-1	-	1	%
Output current	I_{OUT}	V_{IN} = V_{OUT}+1V		500	-	-	mA
Quiescent current	I_Q	V_{IN} = V_{OUT}+1V		-	10	20	μA
Dropout voltage	V_{DO}(7)	I_{OUT} = 500mA		-	200	-	mV
Load regulation	ΔV_{LOAD}	V_{IN} = V_{OUT} +1V, I_{OUT} = 1mA to 500mA		-	20	-	mV
Line regulation	LNR(8)	V_{IN} = V_{OUT} +1V to 5.5V, I_{OUT} = 1mA		-	0.02	-	%/V
Output current limit	I_{CL}	V_{OUT} = 90%$V_{OUT\ Normal}$		-	650	-	mA
Short current	I_{SC}	V_{OUT} = 0 V		-	90	-	mA
Shutdown current	I_{SHDN}	V_{EN} = 0V		-	0.005	1	μA
EN high level	V_{ENH}	Turn on with stable output voltage		1	-	-	V
EN low level	V_{ENL}	Trun off, V_{OUT} = 0V		-	-	0.4	V
EN pin current	I_{EN}	V_{EN} = 5V		-	0.04	0.5	μA
Output noise voltage	V_N(9)	f = 10Hz -100kHz , Vout = 2.8V	I_{OUT} = 1mA	-	5	-	μV_{RMS}
			I_{OUT} = 300mA	-	4.5	-	μV_{RMS}
Power supply rejection ratio	PSRR(9)	I_{OUT} = 10mA, V_{IN} = [V_{OUT} + 1] V + 0.5V_{PP} AC	f = 1kHz	-	98	-	dB
			f = 10kHz	-	91	-	
			f = 100kHz	-	73	-	
			f = 1MHz	-	60	-	
Thermal shutdown	T_{SD}	I_{OUT}=20mA		-	165	-	°C
Thermal shutdown hysteresis	ΔT_{SD}			-	20	-	°C
Active discharge resistance	R_{Dis}	V_{EN} < V_{ENL}		-	900	-	Ω
Turn on time	t_{ON}	From V_{EN} < V_{ENH} to V_{OUT}= 90%$V_{OUT\ Normal}$		-	750	-	μs
Load transient	ΔV_{OUT}(9)	V_{OUT} = 2.8V	I_{OUT} = 1 mA to 500 mA in 1 μs	-	24	-	mV
			I_{OUT} = 500 mA to 1 mA in 1 μs	-	20	-	

Note:

(6) Typical numbers are at 25°C and represent the most likely norm.

(7) Test the difference of output voltage and input voltage when input voltage is decreased gradually till output voltage equals to 98% of $V_{OUT\ Normal}$.

(8) The line regulation is calculated by the following formula:

$$LNR = \frac{\Delta V_{OUT}}{V_{OUT} \times \Delta V_{IN}}$$

where, ΔV_{OUT} is the variation of the output voltage, ΔV_{IN} is the variation of the input voltage.

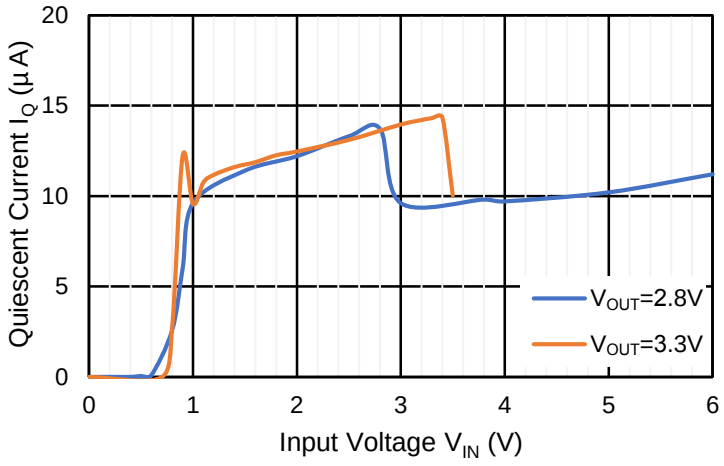
(9) Guaranteed by design, not subject to production testing.

7 Specifications

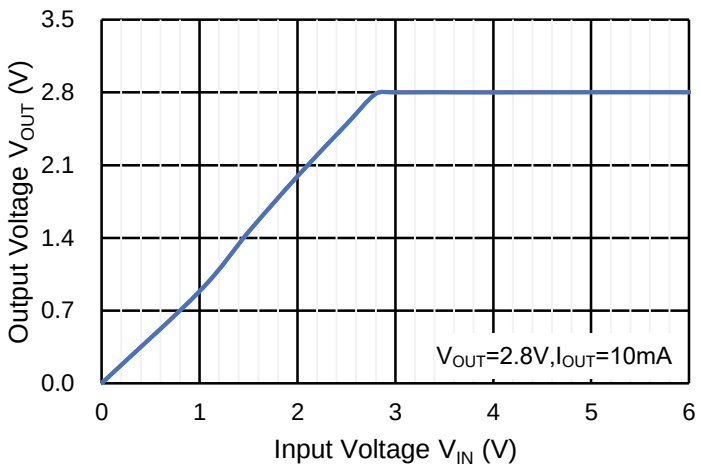
7.6 Typical Characteristics

CJ6216 Series (V_{IN}=V_{OUT}+1V, C_{IN} = 1μF, C_{OUT} = 1μF, T_A = 25°C, unless otherwise specified)

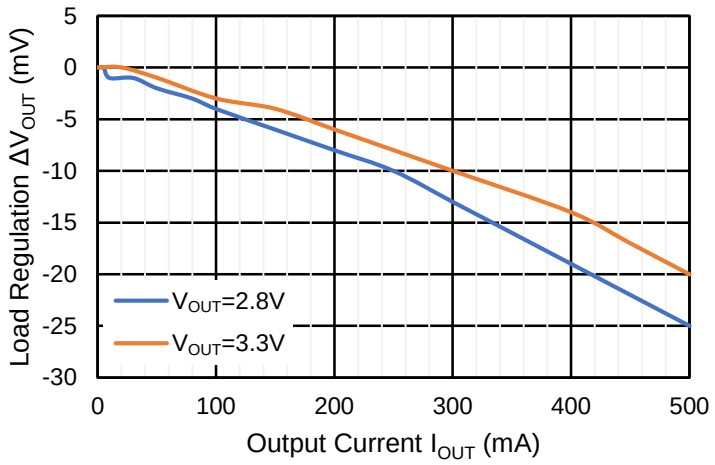
Quiescent Current



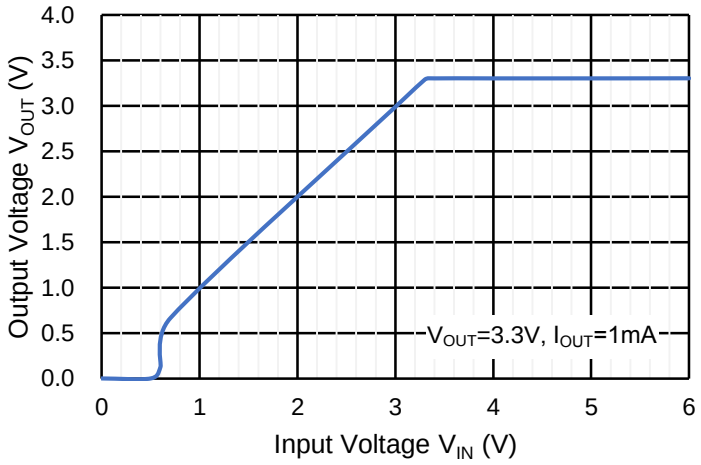
Line Regulation



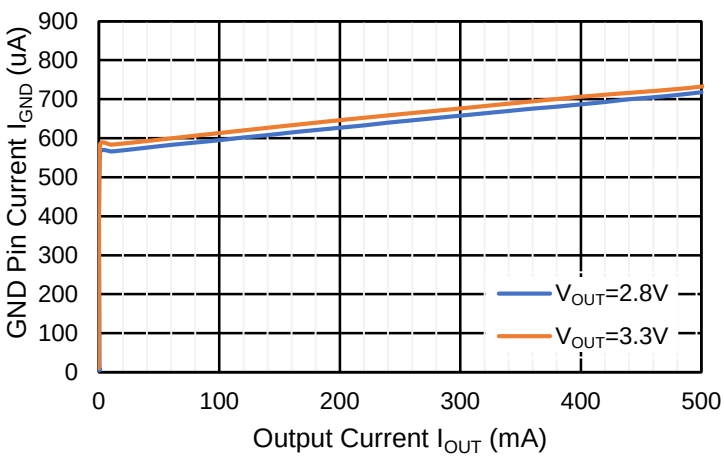
Load Regulation



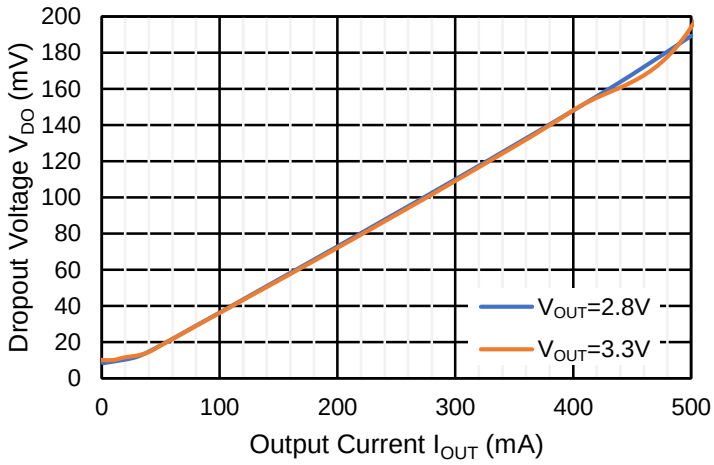
Line Regulation



GND Pin Current vs. I_{OUT}



Dropout Voltage vs. I_{OUT}

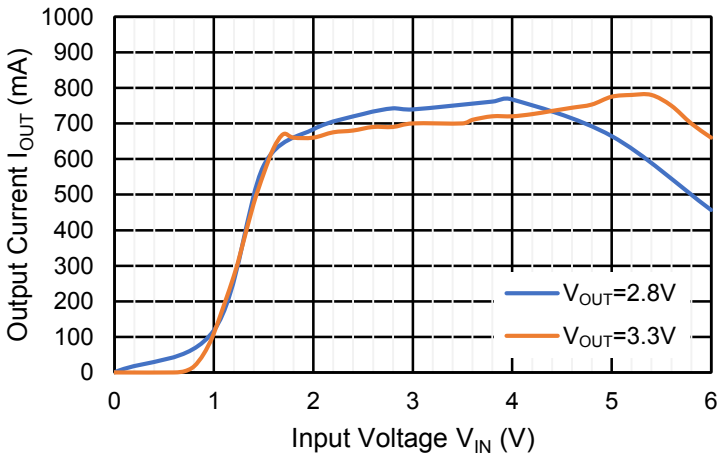


7 Specifications

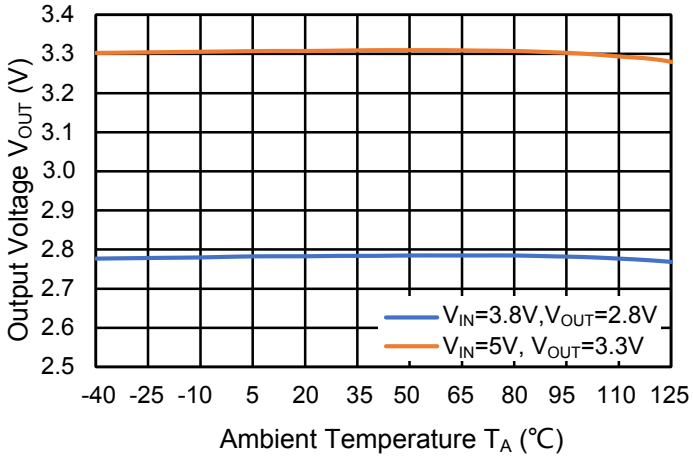
7.6 Typical Characteristics (continued)

CJ6216 Series(V_{IN}=V_{OUT}+1V, C_{IN} = 1μF, C_{OUT} = 1μF, T_A = 25°C, unless otherwise specified)

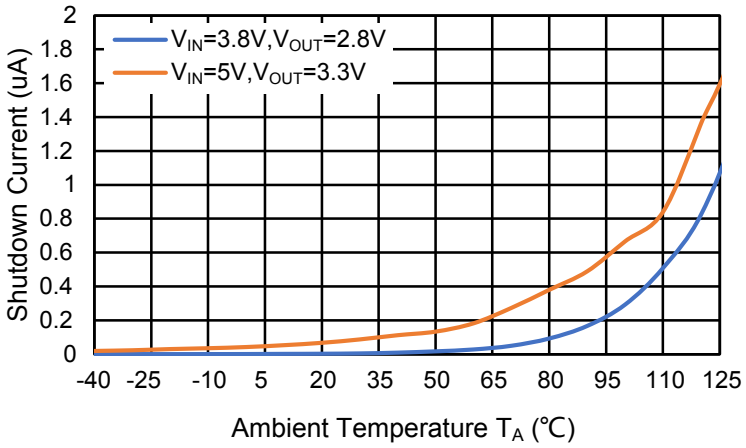
Output Current Limit vs. V_{IN}



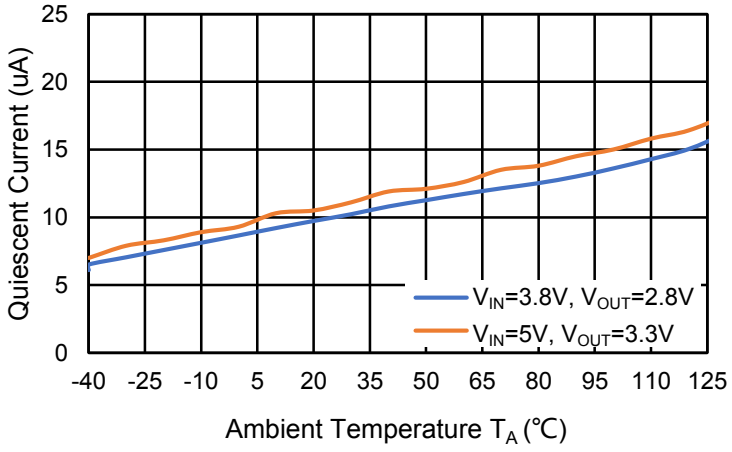
Output Voltage vs. Temperature



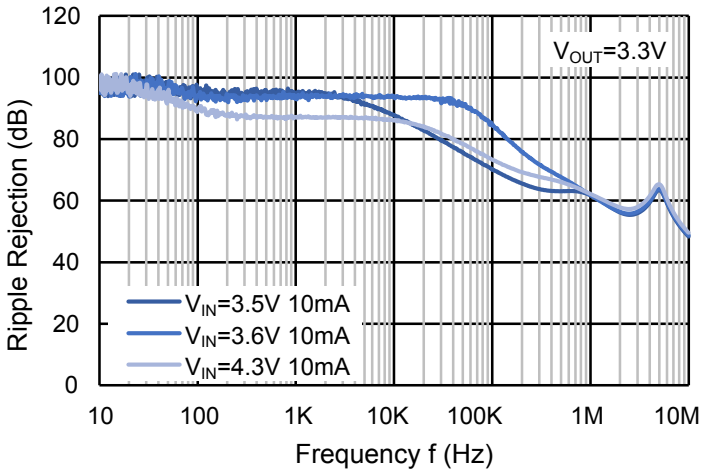
Shutdown Current vs. Temperature



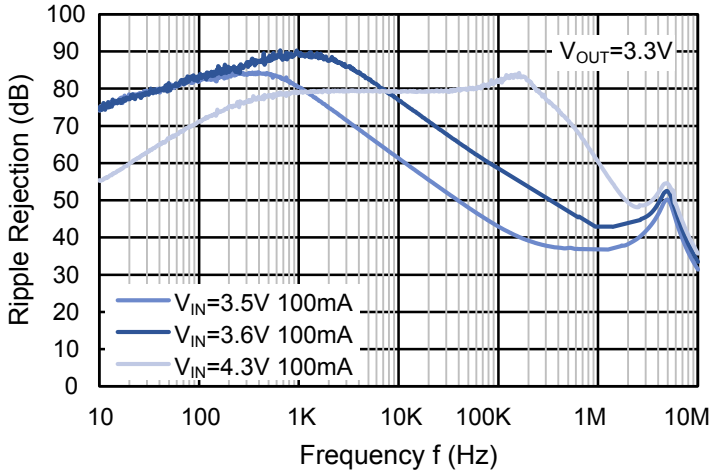
Quiescent Current vs. Temperature



Ripple Rejection vs. Frequence



Ripple Rejection vs. Frequence

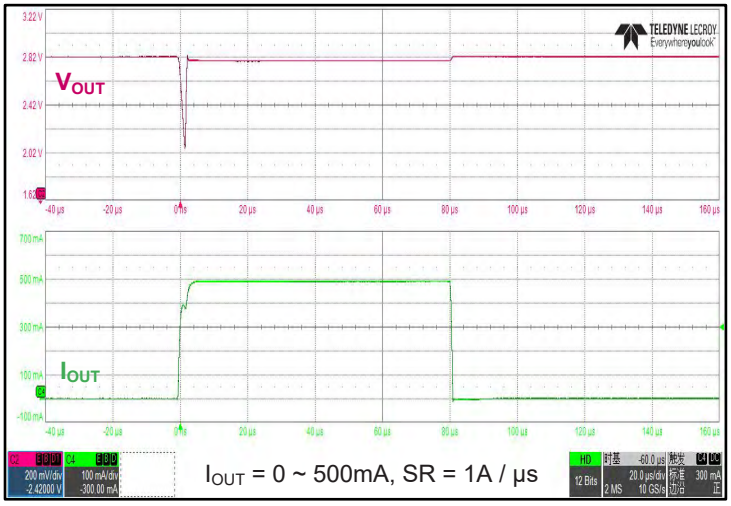


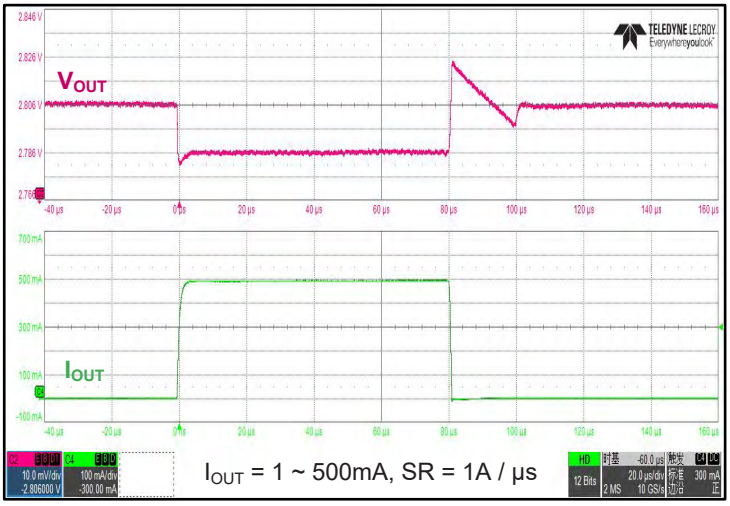
7 Specifications

7.6 Typical Characteristics (continued)

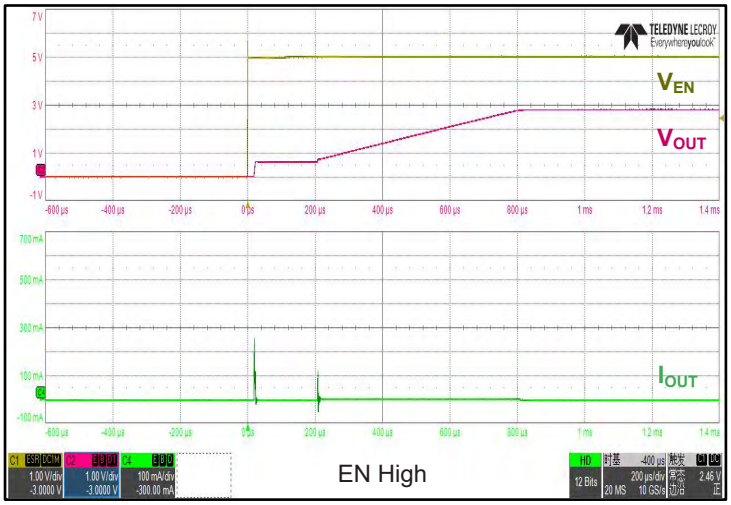
CJ6216 Series (C_{IN} = 1μF, C_{OUT} = 1μF, T_A = 25°C, unless otherwise specified)

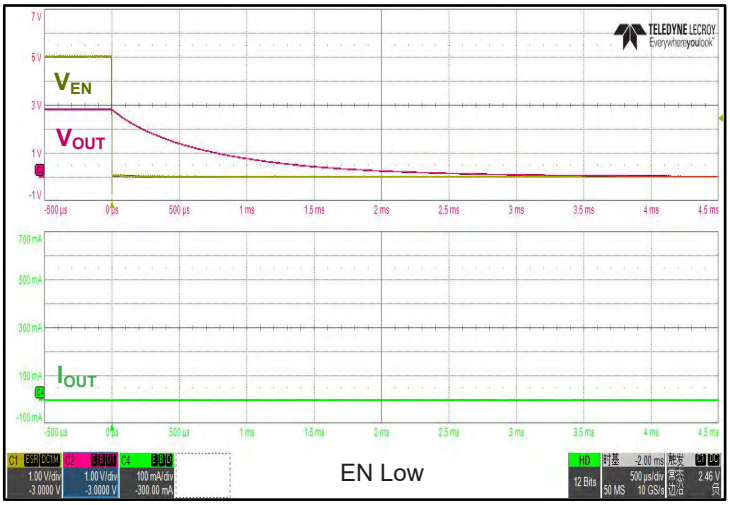
Load Transient (V_{IN} = 3.8V, V_{OUT} = 2.8V,V_{EN} = V_{IN})



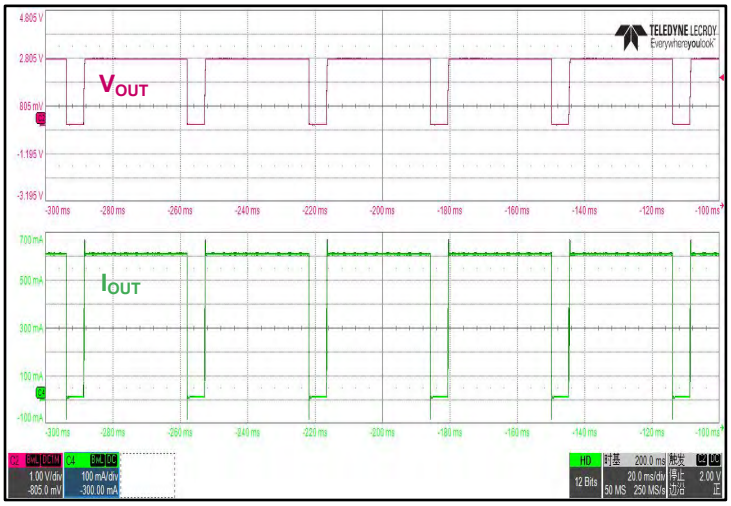


EN High and EN Low(V_{IN} = 3.8V, V_{OUT} = 2.8V,V_{EN} = 0~3.8V)

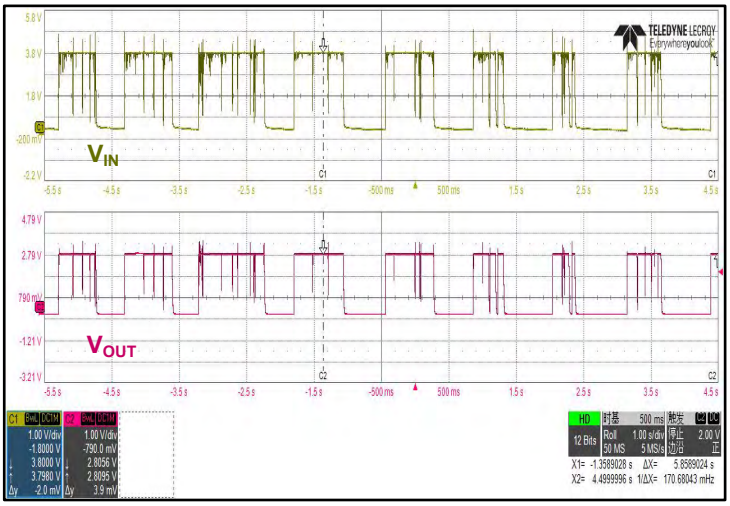




Thermal Shutdown (V_{IN} = 5.2V, I_{OUT} = 600mA)



Continuous Power-up Response

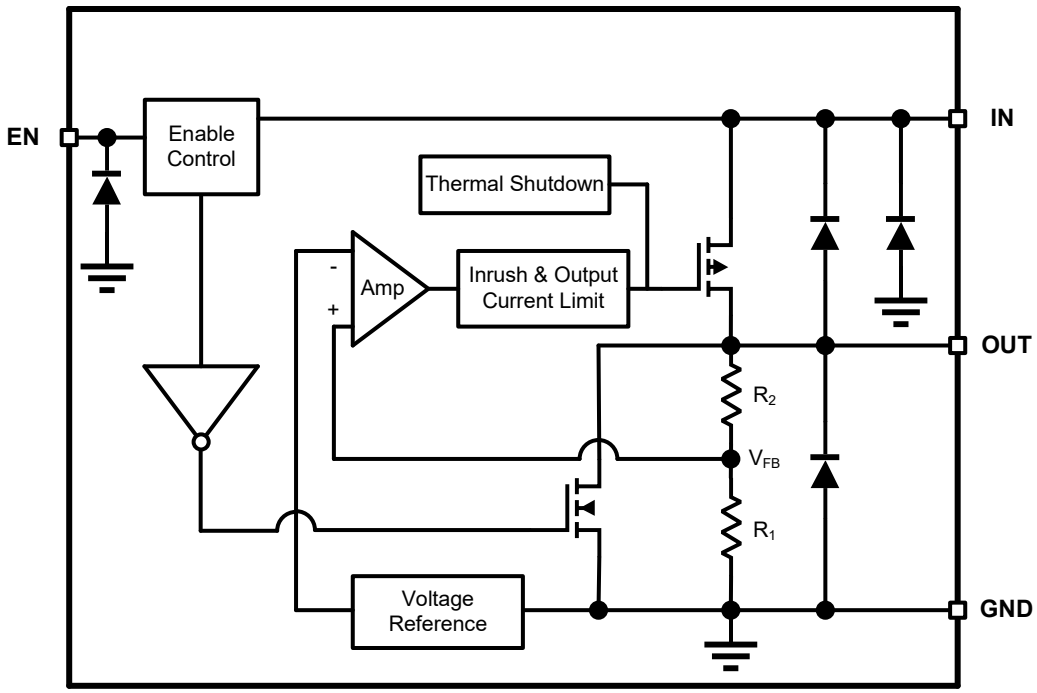


8 Detailed Description

8.1 Description

The CJ6216 Series is a linear regulator capable of supplying 500mA output current. The CJ6216 Series provides low noise, high PSRR, low quiescent current and very good load / line transients. The CJ6216 series has low I_Q performance and is internally integrated with current limiting, short-circuit protection and thermal shutdown protection.

8.2 Functional Block Diagram



The internal feedback resistors R_1 and R_2 form a voltage divider circuit to compare the V_{FB} input error amplifier with the reference voltage. The internal regulator tube (PMOS) will control its conduction degree through the grid voltage provided by the error amplifier output, which will make the output voltage V_{OUT} not affected by temperature changes or input voltage changes to a certain extent, thus maintaining the stability of the device output voltage.

8 Detailed Description

8.3 Feature Description

Power Supply Input

When the input voltage is lower than the rated range of the data sheet, the device will lose the regulation function of stabilizing the output voltage, that is, it is unable to maintain the output voltage within the rated range. At this time, compared with normal operation, the quiescent current of the device may exceed the rated range, and the transient response performance may be seriously degraded.

When the input voltage is higher than the rated range of the data sheet, the device may cause irreversible damage or failure due to exceeding the maximum rated range of electrical stress.

For the rated input voltage of the device, see *Recommended Operating Conditions* and *Dropout Voltage*.

Output Current

When the circuit design is appropriate, the CJ6216 series can reach the maximum load capacity of up to 500mA. According to the power dissipation of the package and the effective connection thermal resistance with the environment, selecting the appropriate package for the circuit design can make the device emit more heat energy.

Output Current Limit & Short Circuit Protection

The CJ6216 series has an internal current limiting circuit, which can protect the device by limiting the load current value in case of instantaneous high load current. When the current limiting is triggered, the output voltage is not regulated. If the out pin of the regulator is short circuited, the internal current limiting circuit will be triggered. The current limiting state will continue until the load current drops to the normal range.

When the load current of the device is large, the device will generate more heat due to the increase of power consumption, which may cause the device to turn off its output due to the internal thermal shutdown protection before the current limit is triggered. After the device cools down, the internal thermal shutdown circuit turns the device back on. If the fault condition continues, the device cycles between current limit and thermal shutdown.

In order to ensure the normal operation of current limit, the inductance of input and load shall be minimized. Continuous operation under current limit is not recommended.

Thermal Shutdown

The CJ6216 series has thermal shutdown protection mechanism. When the junction temperature T_J of the internal main channel MOSFET exceeds the thermal shutdown threshold temperature T_{SD}, thermal shutdown will be triggered. At this time, the output will be turned off to prevent catastrophic damage to the chip due to accidental heating. When the T_J drops to a certain range of thermal shutdown threshold temperature ΔT_{SD}, the thermal shutdown will be released and the device will return to the normal output. The temperature threshold of device triggering thermal shutdown T_{SD} and temperature range to be lowered to released from thermal shutdown ΔT_{SD} can be found in the *Electrical Characteristics*.

To ensure reliable operation, please limit the junction temperature to the specified range of *Recommended Operating Conditions* in the data sheet. Applications that exceed the recommended temperature range may cause the device to exceed its operating specifications.

Although the internal protection circuit of the device is designed to prevent overall thermal conditions, it is not intended to replace proper power dissipation. Running the device continuously until thermal shutdown or higher than the recommended operating T_J will reduce long-term reliability.

8 Detailed Description

8.3 Feature Description (continued)

Dropout Voltage

Dropout voltage V_{DO} refers to the minimum voltage difference between input and output V_{IN} - V_{OUT} to make the device output voltage reach the rated range at rated current. When the dropout voltage condition required by the device is reached, the internal MOSFET will be fully turned on, at this time, the MOSFET is equivalent to a switch for regulation.

The V_{DO} increases with the increase of load current. Since V_{IN} - V_{OUT} must be no less than the V_{DO}, the V_{DO} indirectly specifies the minimum input voltage of devices under different load current conditions. If the V_{IN} - V_{OUT} is less than the V_{DO}, the performance of the device may deteriorate (see *Operation in Dropout Mode* for details).

Enable Control

The enable pin of the device EN is active at high level. When the voltage of the EN is greater than the EN logic high voltage $V_{EN\ H}$, the device will be enabled and maintain the normal output. When the voltage of the EN is lower than the EN logic low voltage $V_{EN\ L}$, the internal circuit of the device will be disabled and the output will be turned off, the device will be in the standby mode until EN is turned to high level again. The $V_{EN\ H}$ and $V_{EN\ L}$ can be found in the *Electrical Characteristics*.

Normal startup waveform and startup slope rate control can be ensured when the device starts from any low voltage lower than $V_{EN\ L}$, but the discharge time of output capacitor must be taken into account. EN can be float, when the EN is floating, it will be pulled down to ground internally. If EN is not required to control the output voltage independently, it's recommend to connect EN to IN.

Auto-discharge Function

The device with enable control has an auto-discharge circuit. When the enable control is turned off, the device will be disabled. An internally integrated pull-down MOSFET (see *Functional Block Diagram*) will connect a resistor $R_{Discharge}$ to the ground to release the charge in the output capacitor, thus closing the entire device circuit. The value of $R_{Discharge}$ can be found in the *Electrical Characteristics*. The discharge time of the output capacitor after the device is disabled is determined by the output capacitance C_{OUT} and load resistance R_L in parallel with the $R_{Discharge}$. The time constant τ can be calculated by the following formula:

$$\tau = C \times R_{Discharge} \ (R_L = 0)$$

$$\tau = C \times \left(\frac{R_L \times R_{Discharge}}{R_L + R_{Discharge}} \right) (R_L \neq 0)$$

The output voltage after discharging through pull-down MOSFET can be calculated by the following formula:

$$V = V_{OUT} \times e^{-\frac{t}{\tau}}$$

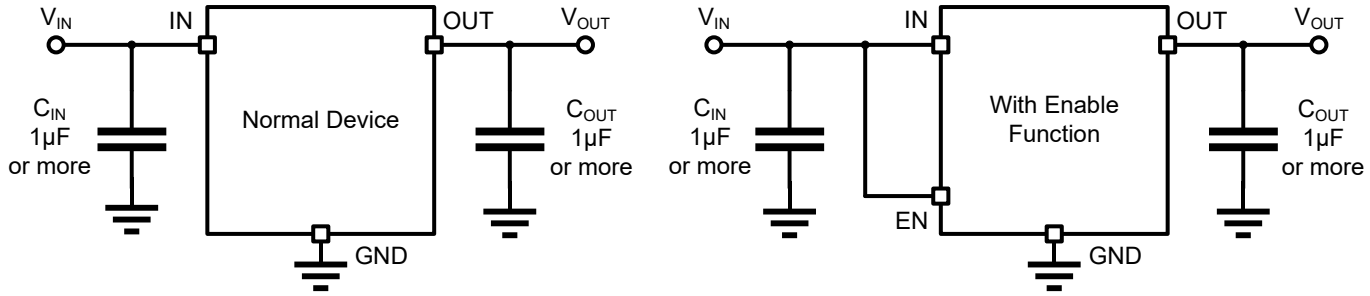
$$t = \tau \times \ln(\frac{V}{V_{OUT}})$$

Where, V is the output voltage after discharge, V_{OUT} is the output voltage, t is the discharge time, τ is the discharge time constant.

Do not rely on the active discharge circuit to release a large amount of output capacitance after the input power supply crashes, because the reverse current can flow from the output to the input. This reverse current may damage the device. The limiting reverse current shall not exceed 5% of the rated current of the device.

9 Application and Implementation

9.1 Typical Application Circuits



9.2 Application Information

Selection of Bypass Capacitances

For the CJ6216 series, it is recommended to use 1μF input (C_{IN}) and output (C_{OUT}) ceramic capacitors.

Type of Capacitors:

Since any leakage of the capacitor will increase the quiescent power consumption of the whole circuit, attention should be paid to selecting capacitors with low leakage. When designing the circuit of portable equipment including CJ6216 series, due to the shortage of tantalum capacitors, it is a good choice to use small size, low equivalent series resistance (ESR) and high RMS current capacity multilayer ceramic capacitors (MLCC) in the DC to DC voltage conversion. The designer must choose the appropriate capacitor type for circuit design: X7R- Ceramic capacitors of X5R- and COG- rated dielectric materials can provide relatively good capacitance stability within the temperature range, Y5V- type capacitors are not recommended because of large changes in capacitance values. However, no matter which type of ceramic capacitor is selected, the effective capacitance may vary with the operating voltage and temperature. The designer must consider the influence of the change of the effective value of capacitance according to the circuit design and application conditions.

Input Capacitors C_{IN}:

It is recommended to use a 1μF capacitor at the input pin of the device, and the position of the input capacitor should be as close to the device input pin as possible.

For the CJ6216 series, the input capacitor is not necessary to maintain the output stability, but it can offset the reactive input source and improve the transient response, input ripple and PSRR performance of the device. It should be noted that although many types of capacitors can be used for input bypass, using ceramic capacitors for input filtering may cause problems. Due to the self resonance and high Q characteristics of some types of ceramic capacitors, under certain starting conditions, applying voltage steps to ceramic capacitors may lead to large current surges (such as directly connecting the input pin of LDO to the power supply), which may cause some energy stored in the parasitic inductance of the power lead. When the stored energy is transferred from these inductors to ceramic capacitors, large voltage spikes may occur in the circuit. These voltage spikes are easily twice the step amplitude of the input voltage, and are likely to bring potential risks to the normal operation and reliability of the device. Therefore, the selection of ceramic capacitors as input capacitors must be careful. Adding 3Ω resistors and X5R- type ceramic capacitors will minimize voltage transients during startup. A higher value capacitor may be necessary if large, fast rise-time load or line transients are anticipated or if the device is located several inches from the input power source.

9 Application and Implementation

9.2 Application Information (continued)

Selection of Bypass Capacitances (continued)

Output Capacitors C_{OUT}:

Recommended 1μF output ceramic capacitor to keep the device output stable, and the capacitor position should be as close to the device pin as possible.

For CJ6216 series, the device needs an output capacitor to achieve loop stability. As with any regulator, a larger output capacitance reduces the peaks during a load transient but slows down the response time of the device. The proper capacitor can help to obtain better dynamic performance.

Transient Response

Transient response refers to the change of system output from initial state to stable state under the action of typical signal input. For LDO, the designer should pay attention to the possible impact of linear transient response and load transient response on the system: linear transient response refers to the transient response of output to change when the input voltage changes, while load transient response refers to the transient response of output to change when the output current changes. The specific phenomenon is that the output voltage of the device will have a short spike, especially when the input voltage or output current changes greatly in a short time. This change is not only related to the performance of the chip itself, but also related to the change of output current, change rate and output capacitance:

1. When the output current increases, the output voltage of the device will decrease to a certain extent, and the larger output current will provide a higher current discharge path for the output capacitor, which will affect the peak value generated by the transient spike and reduce the peak value;
2. The output current or input voltage changes relatively slowly, and the output change of the device is relatively small, affecting the spike caused by the change;
3. The use of large input and output capacitors can reduce the spike caused by transient response to a certain extent to improve the transient performance, but large output capacitors can also affect the response time of devices.

For the selection of bypass capacitance value, refer to the *Section of Bypass Capacitances* selection.

Operation in Dropout Mode

The CJ6216 series is internally integrated with a P-MOSFET to achieve low dropout voltage. The voltage difference between the input and the output V_{IN} - V_{OUT} of the device must not be lower than the corresponding dropout voltage V_{DO} to ensure that the output voltage tolerance is within the rated range of the data sheet. The dropout voltage will increase with the increase of load current. When the V_{IN} - V_{OUT} is less than the V_{DO}, the P-MOSFET inside the device is in a linear state, the resistance from the input pin to the output pin is equal to the resistance from the drain to the source of the P-MOSFET, and the device functions like a resistor. When operating in this state, the response time of the error amplifier inside the device will be limited, which will seriously degrade the transient performance of the device, when the external circuit has a transient change, the deviation of the output voltage will become larger than the normal operating state. In addition, the PSRR and noise performance of the device will be worse than that under normal operating conditions.

9 Application and Implementation

9.2 Application Information (continued)

Recommended Continuous Operating Areas

As an LDO, the working area of CJ6216 series is limited by dropout voltage, output current, junction temperature and input voltage under continuous working condition. The recommended areas for continuous operation are shown in Figure 9-5:

A. The LDO input and output voltage difference V_{IN} - V_{OUT} must meet the dropout voltage V_{DO} conditions. See *Dropout Voltage* for more details.
B. Rated output current range I_{Rated}.
C. The actual junction temperature T_J of LDO shall not exceed the rated junction temperature. The product of voltage difference and current at both ends of LDO is power consumption, which determines the actual working junction temperature of LDO, so the curve is not linear.

In addition, the working area of CJ6216 series is limited by the rated $V_{IN\ MIN}$ and $V_{IN\ MAX}$.

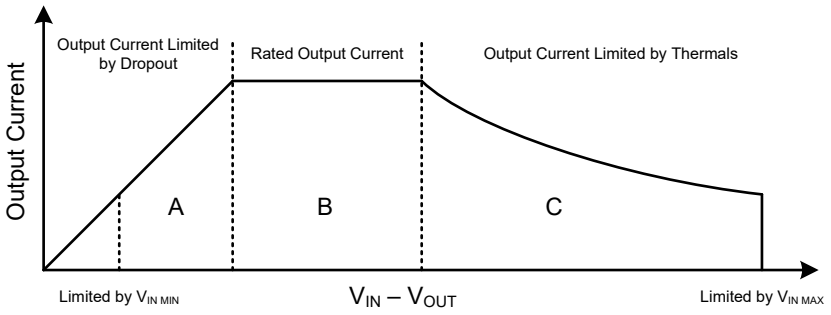


Figure 9-5. Region Description for Continuous Operation

9.3 Power Supply Recommendation

The CJ6216 series is designed to operate within the input power supply voltage range of 1.0V to 6.0V. The input power supply should be well adjusted and have low noise. If the input power supply has high noise, it is recommended to use an additional bypass capacitor at the input to improve the output noise performance of the device. It is recommended to use an input capacitor of 1μF or higher to reduce the impedance of the input power supply, especially during transients.

9.4 Layout Guidelines

When designing the circuit including CJ6216 series, the following matters should be noted:

- Place the input and output capacitors as close to the pins of the device as possible;
- The device is connected by copper plane and the heat sink (or back pad) of the device is fully welded with PCB to obtain better heat dissipation performance and lower on resistance;
- Heat sink holes are placed around the device to help the circuit dissipate more heat energy. However, attention should be paid to the position of the heat sink holes to prevent the solder (or solder paste) on the IC pad from being absorbed by the heat sink holes and being damaged during welding.

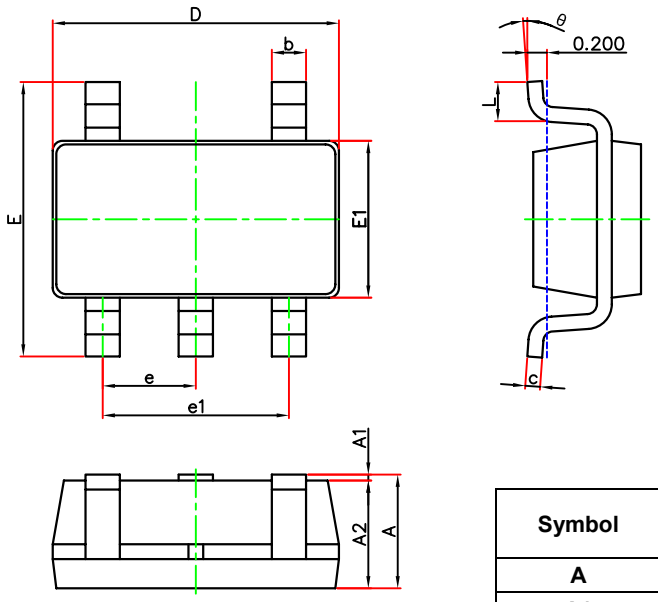
NOTE

The application information in this section is not part of the data sheet component specification, and SZCJ makes no commitment or statement to guarantee its accuracy or completeness. Customers are responsible for determining the rationality of corresponding components in their circuit design and making tests and verifications to ensure the normal realization of their circuit design.

10 Mechanical Information

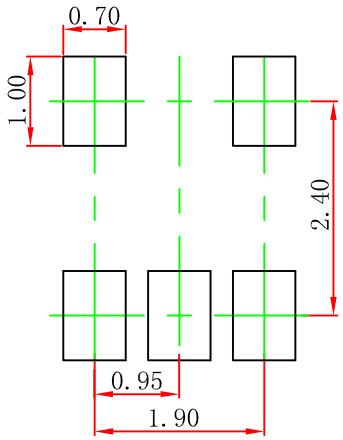
10.1 SOT-23-5L Mechanical Information

SOT-23-5L Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	2.650	2.950	0.104	0.116
E1	1.500	1.700	0.059	0.067
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

SOT-23-5L Suggested Pad Layout



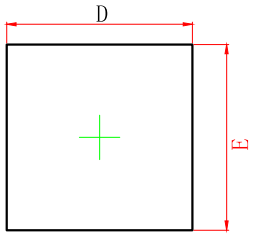
Note:

1. Controlling dimension: in millimeters.
2. General tolerance: ±0.05mm.
3. The pad layout is for reference purpose only.

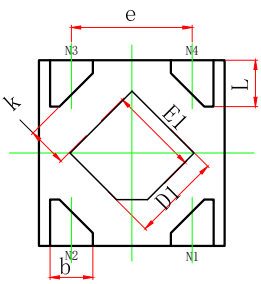
10 Mechanical Information

10.2 DFNWB1×1-4L Mechanical Information

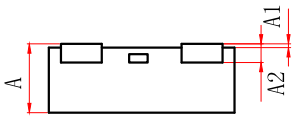
DFNWB1×1-4L Outline Dimensions



TOP VIEW



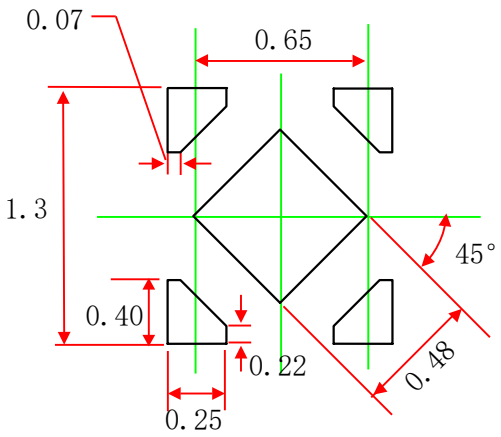
BOTTOM VIEW



SIDE VIEW

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.320	0.400	0.013	0.016
A1	0.000	0.050	0.000	0.002
A2	0.100 REF.		0.004 REF.	
D	0.950	1.050	0.037	0.041
E	0.950	1.050	0.037	0.041
D1	0.430	0.530	0.017	0.021
E1	0.430	0.530	0.017	0.021
k	0.150MIN.		0.006MIN.	
b	0.180	0.280	0.007	0.011
e	0.650TYP.		0.026TYP.	
L	0.200	0.300	0.008	0.012
L1	0.200	0.300	0.008	0.012

DFNWB1×1-4L Suggested Pad Layout



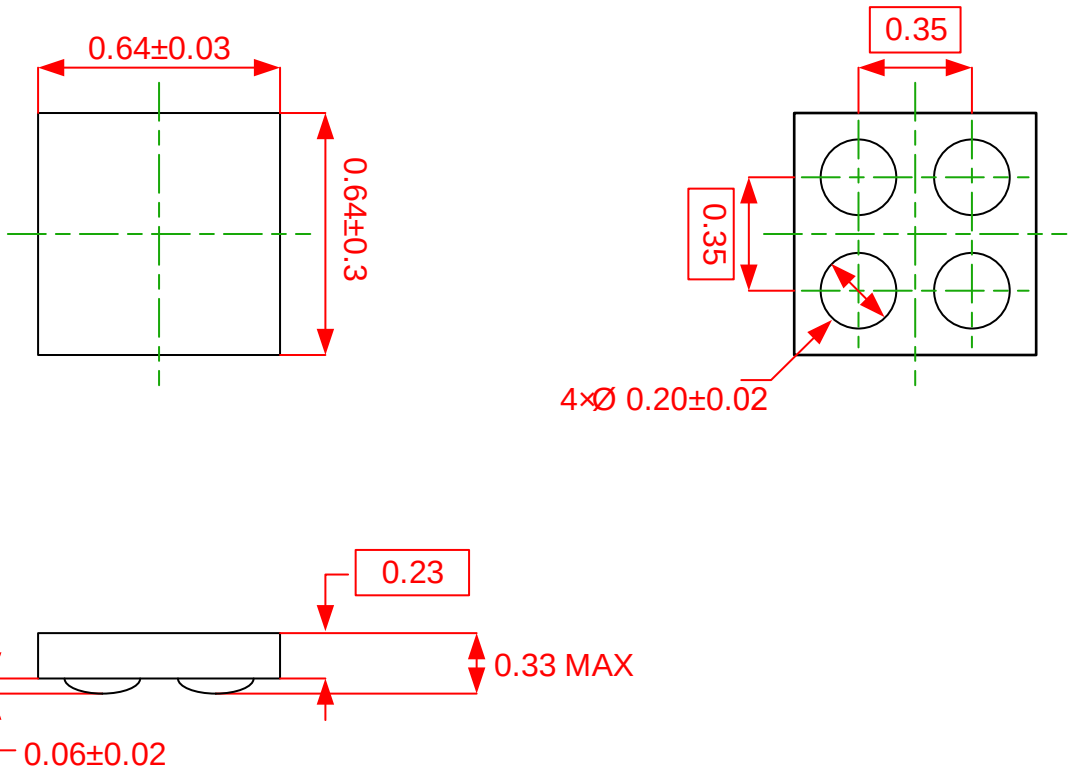
Note:

1. Controlling dimension: in millimeters.
2. General tolerance: ±0.05mm.
3. The pad layout is for reference purpose only.

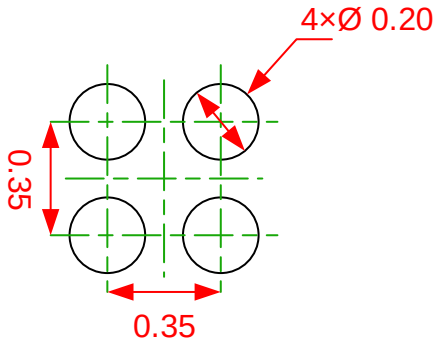
10 Mechanical Information

10.3 CSPC0606-4 Mechanical Information

CSPC0606-4 Outline Dimensions



CSPC0606-4 Suggested Pad Layout



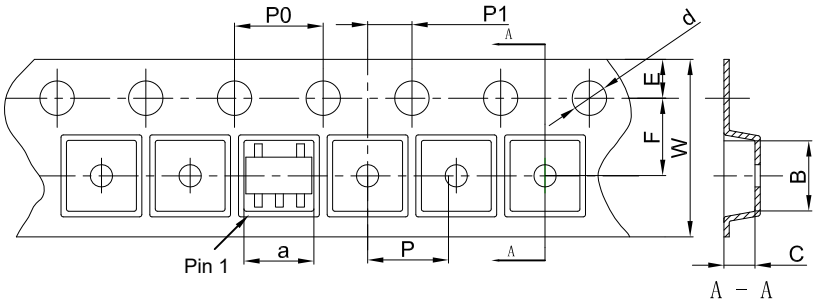
Note:

1. Controlling dimension: in millimeters.
2. General tolerance: ±0.05mm.
3. The pad layout is for reference purpose only.

11 Packaging Information

11.1 SOT-23-5L Tape and Reel Information

SOT-23-5L Tape and Reel Information

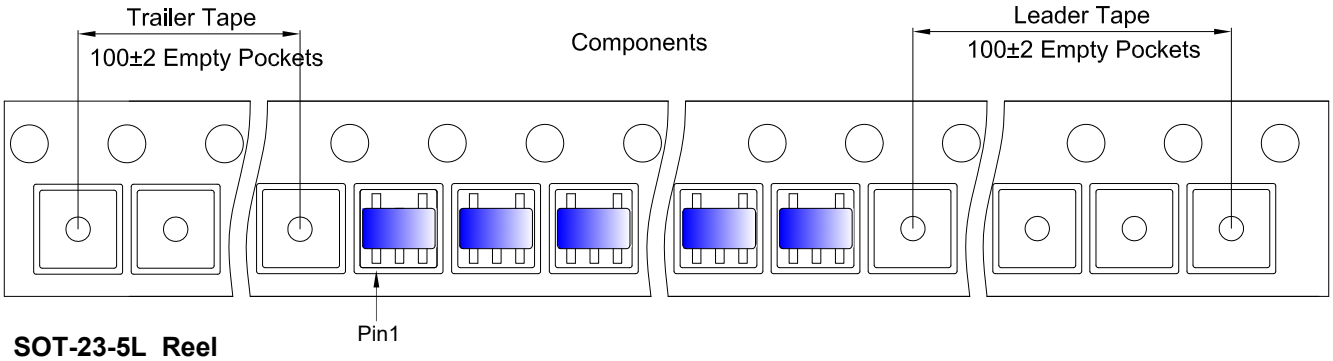


Packaging Description:

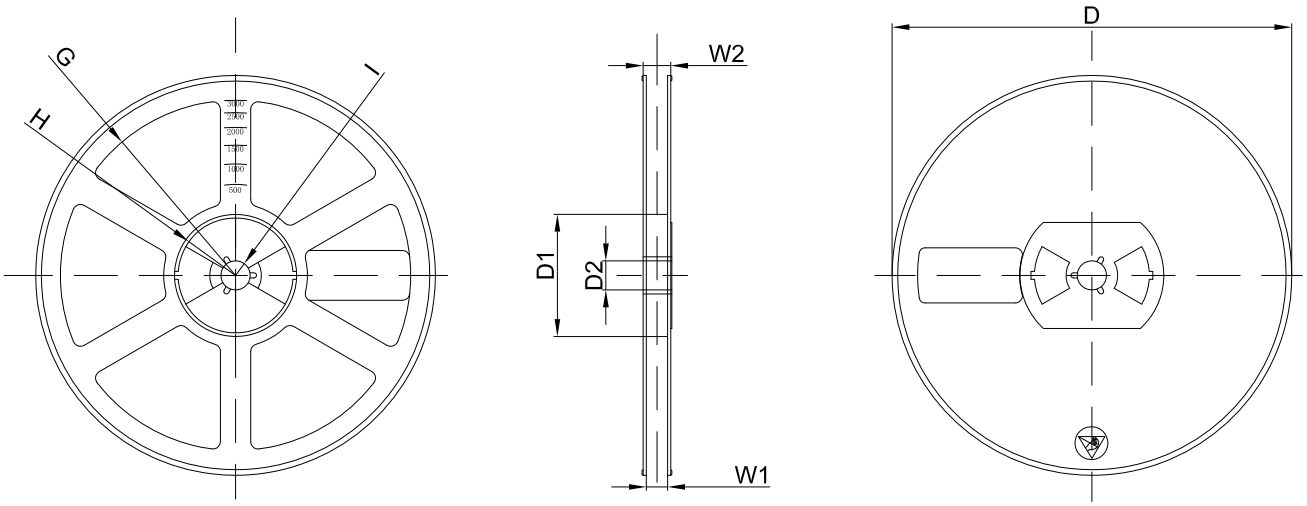
SOT-23-5L parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 3,000 units per 7" or 18.0cm diameter reel. The reels are clear in color and is made of polystyrene plastic (anti-static coated).

Dimensions are in millimeter										
Pkg type	a	B	C	d	E	F	P0	P	P1	W
SOT-23-5L	3.17	3.23	1.37	Ø1.55	1.75	3.50	4.00	4.00	2.00	8.00

SOT-23-5L Tape Leader and Trailer



SOT-23-5L Reel



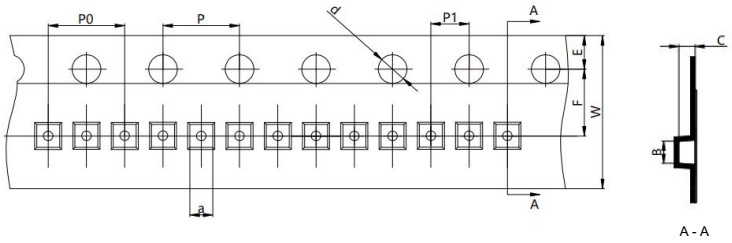
Dimensions are in millimeter								
Reel Option	D	D1	D2	G	H	I	W1	W2
7"Dia	Ø180.00	60.00	13.00	R78.00	R25.60	R6.50	9.50	13.10

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)	G.W.(kg)
3000 pcs	7 inch	30,000 pcs	203×203×195	120,000 pcs	438×438×220	

11 Packaging Information

11.2 DFNWB1×1-4L Tape and Reel Information

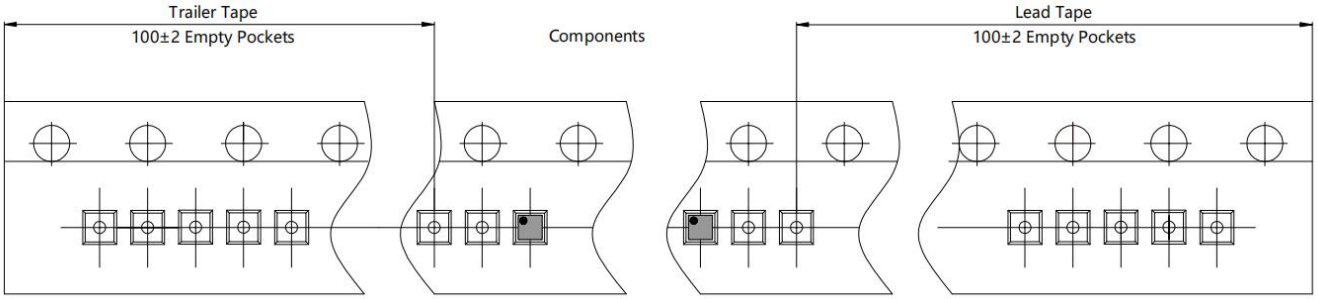
DFNWB1×1-4L Embossed Carrier Tape



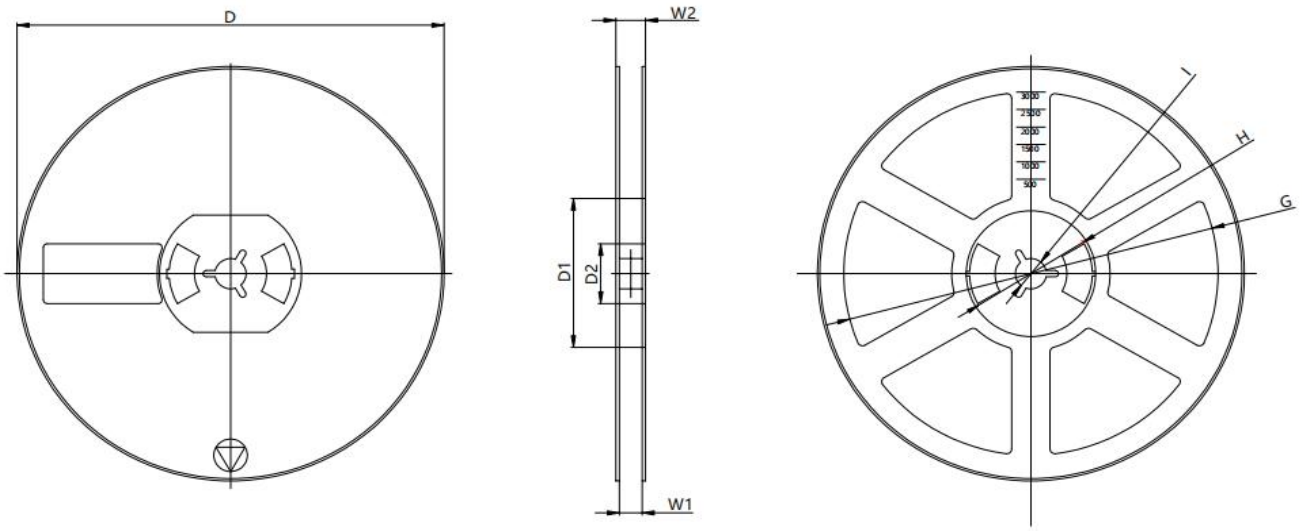
Packaging Description:
DFNWB1×1-4L parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled)polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 3,000 units per 7"or 18.0cm diameter reel. The reels are clear in color and is made of polystyrene plastic (anti-static coated).

Dimensions are in millimeter										
Pkg type	a	B	C	d	E	F	P0	P	P1	W
DFNWB1×1-4L	1.12	1.13	0.50	1.55	1.75	3.5	4.00	4.00	2.00	8.00

DFNWB1×1-4L Tape Leader and Trailer



DFNWB1×1-4L Reel



Dimensions are in millimeter								
Reel Option	D	D1	D2	G	H	I	W1	W2
7''Dia	Ø 178.00	54.5	13.5	R78.0	R25.6	R6.75	9.6	12.3

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)	G.W.(kg)
10,000 pcs	7 inch	100,000pcs	210×208×205	400,000 pcs	440×440×230	

12 Notes and Revision History

12.1 Associated Product Family and Others

To view other products of the same type or IC products of other types, click the official website of SZCJ -- https: ***www.***szcjsemi.com for more details.

12.2 Notes

Electrostatic Discharge Caution



This IC may be damaged by ESD. Relevant personnel shall comply with correct installation and use specifications to avoid ESD damage to the IC. If appropriate measures are not taken to prevent ESD damage, the hazards caused by ESD include but are not limited to degradation of integrated circuit performance or complete damage of integrated circuit. For some precision integrated circuits, a very small parameter change may cause the whole device to be inconsistent with its published specifications.

12.3 Revision History

May, 2025: changed from rev - 1.1 to rev - 1.2:

• Page 2~3, Orderable Information, changed OP TEMP from -40 ~ 85°C to -40 ~ 125°C.

• Page 6, Recommended Operating Conditions, removed Operated ambient temperature T_A.

March, 2025: changed from rev - 1.0 to rev - 1.1:

• Page 9 ~ 10, Typical Characteristics, added the typical characteristics chart of CJ6216-3.3V device.

September, 2024: released CJ6216 series rev - 1.0.

DISCLAIMER

IMPORTANT NOTICE, PLEASE READ CAREFULLY

The information in this data sheet is intended to describe the operation and characteristics of our products. SZCJ has the right to make any modification, enhancement, improvement, correction or other changes to any content in this data sheet, including but not limited to specification parameters, circuit design and application information, without prior notice.

Any person who purchases or uses SZCJ products for design shall: 1. Select products suitable for circuit application and design; 2. Design, verify and test the rationality of circuit design; 3. Procedures to ensure that the design complies with relevant laws and regulations and the requirements of such laws and regulations. SZCJ makes no warranty or representation as to the accuracy or completeness of the information contained in this data sheet and assumes no responsibility for the application or use of any of the products described in this data sheet.

Without the written consent of SZCJ , this product shall not be used in occasions requiring high quality or high reliability, including but not limited to the following occasions: medical equipment, military facilities and aerospace. SZCJ shall not be responsible for casualties or property losses caused by abnormal use or application of this product.

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