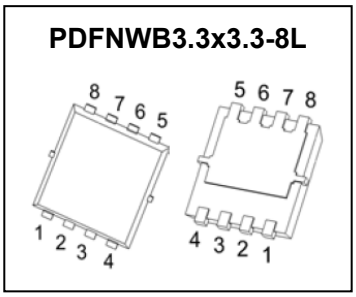
SZCJ

DFNWB3.3x3.3-8L Plastic-Encapsulate MOSFET

010SN03A N-Channel Power MOSFET



Key Performance Parameters

$V_{BR(DSS)}$	$R_{DS(on)}$TYP	I_D
30V	7.5mΩ@10V	35A
	12.5mΩ@4.5V	

DESCRIPTION

The N-Channel enhancement mode power field effect transistors is using SGT technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.These devices are well suited for high efficiency fast switching applications.

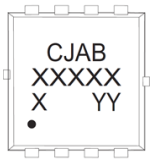
FEATURES

- 100% Avalanche tested
- Low drain-source on-resistance
- Low gate charge
- High current capability

APPLICATIONS

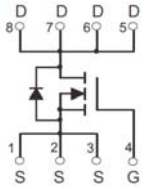
- DC/DC
- Switching application

MARKING



XXXXXX = 010SN03A
Solid dot = Pin1 indicator.
YY = Code.

EQUIVALENT CIRCUIT



ABSOLUTE MAXIMUM RATINGS (T_J=25℃ unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	T_C =25℃	I_D	35	A
	T_C =100℃		22	
Pulsed Drain Current		I_{DM}①②	140	A
Continuous Drain Current	T_A =25℃	I_D⑥	16	A
	T_A =75℃		12.4	
Avalanche Current		I_{AS}③	7.5	A
Single Pulsed Avalanche Energy		E_{AS}③	14	mJ
Power Dissipation		P_D①	34.7	W
Operating Junction and Storage Temperature Range		T_J,T_{stg}	-55~+150	℃

Thermal Characteristics

Parameter		Symbol	Value		Unit
			Typ	Max	
Thermal Resistance from Junction to Case		$R_{\theta JC}$	2.4	3.6	℃/W
Thermal Resistance from Junction to Ambient	t≤10s	$R_{\theta JA}$⑥	20	30	℃/W
	Steady State		46	70	℃/W

Typical Characteristics

ELECTRICAL CHARACTERISTICS (T_J=25℃ unless otherwise specified)

Static Characteristics

Parameter	Symbol	Test Condition		Value			Unit
				Min	Typ	Max	
Drain-source breakdown voltage	$V_{(BR)DSS}$	V_{GS}=0V, I_D=250μA		30	-	-	V
Zero gate voltage drain current	I_{DSS}	V_{DS}=30V, V_{GS}=0V	T_J=25℃	-	-	1.0	μA
			T_J=125℃	-	-	100	
Gate-body leakage current	I_{GSS}	V_{GS}=±20V, V_{DS}=0V		-	-	±100	nA
Gate-threshold voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250μA		1.3	1.8	2.3	V
Static drain-source on-sate resistance	$R_{DS(on)}$④	V_{GS}=10V, I_D=20A	T_J=25℃	-	7.5	10	mΩ
			T_J=125℃	-	11	14.8	
		V_{GS}=4.5V, I_D=20A		-	12.5	17.5	
Forward transconductance	g_{FS}	V_{DS}=5V, I_D=20A		-	37	-	S

Dynamic Characteristics⑤

Input capacitance	C_{iss}	V_{GS}=0V, V_{DS}=15V, f=1MHz	-	393	-	pF
Output capacitance	C_{oss}		-	308	-	
Reverse transfer capacitance	C_{rss}		-	24	-	
Gate resistance	R_g	f=1MHz	-	2.0	-	Ω
Total gate charge	Q_g	V_{GS}=4.5V, V_{DS}=15V, I_D=20A	-	2.6	-	nC
Total gate charge	Q_g	V_{GS}=10V, V_{DS}=15V, I_D=20A	-	5.6	-	
Gate charge at threshold	$Q_{G(th)}$		-	0.7	-	
Gate-source charge	Q_{gs}		-	1.3	-	
Gate-drain charge	Q_{gd}		-	0.8	-	
Turn-on delay time	$t_{d(on)}$	V_{DD}=15V, V_{GS}=10V, I_D=10A, R_g=3Ω	-	2.2	-	ns
Turn-on rise time	t_r		-	1.3	-	
Turn-off delay time	$t_{d(off)}$		-	5.8	-	
Turn-off fall time	t_f		-	1.1	-	

Reverse Diode Characteristics

Drain-source diode forward voltage	V_{SD}④	V_{GS}=0V, I_S=20A	-	-	1.2	V
Continuous drain-source diode forward current	I_S①		-	-	35	A
Pulsed drain-source diode forward current	I_{SM}①②		-	-	140	A
Reverse recovery time	t_{rr}	V_{DD}=15V, I_S=20A, di/dt=500A/μs	-	9.1	-	ns
Reverse recovery charge	Q_{rr}		-	9.0	-	nC

Notes:

①.T_C=25℃ Limited only by maximum temperature allowed.

②.P_W ≤10μs, Duty cycle ≤1%.

③.EAS condition: V_{DD}=20V, V_{GS}=10V, L=0.5mH, R_g=25Ω Starting T_J=25℃.

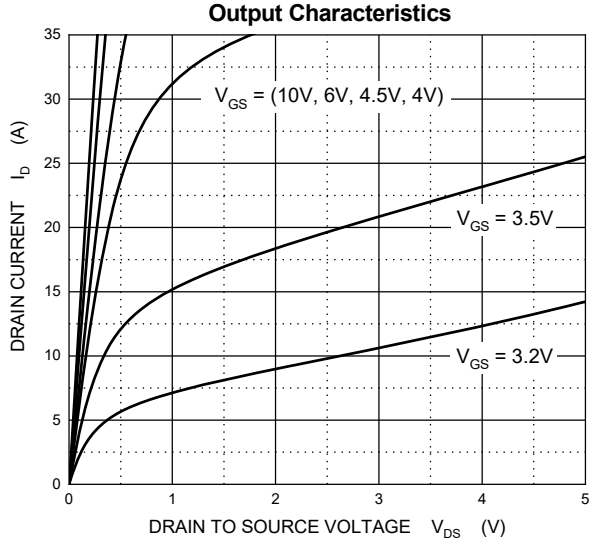
④.Pulse Test : Pulse Width ≤380μs, duty cycle ≤2%.

⑤.Guaranteed by design, not subject to production.

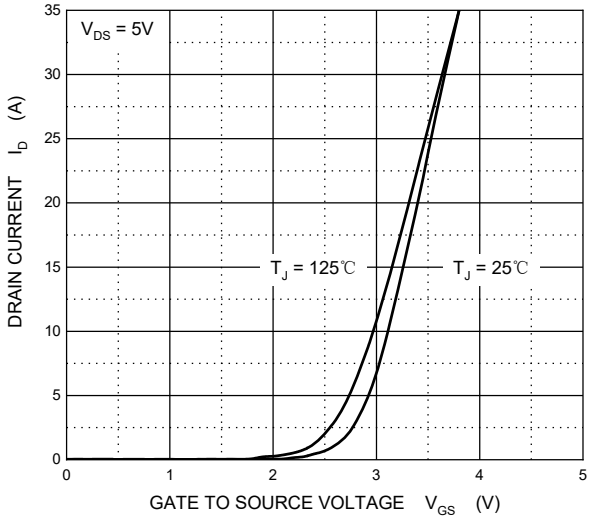
⑥.Device mounted on 1 in^2 FR-4 board with 2oz. double-sided Copper, in a still air environment with T_A=25℃.The current rating is based on the t ≤10s thermal resistance rating.

Typical Characteristics

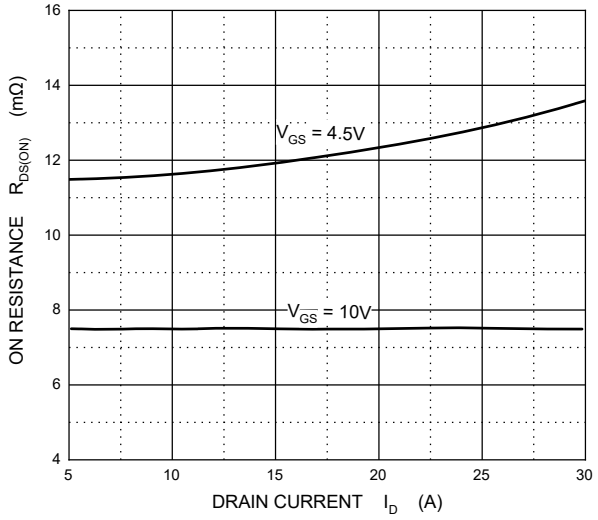
Output Characteristics



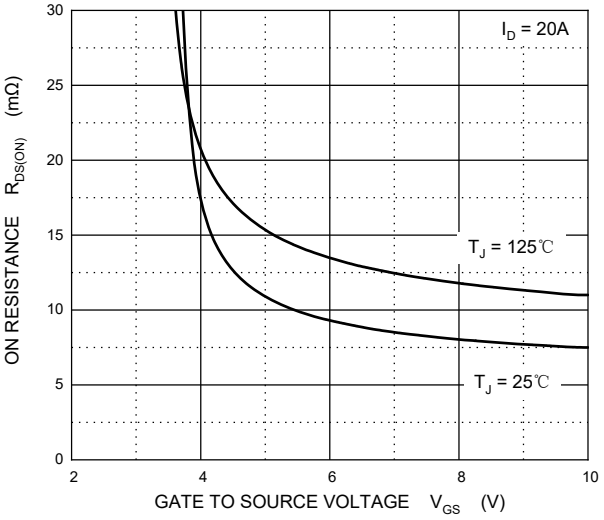
Transfer Characteristics



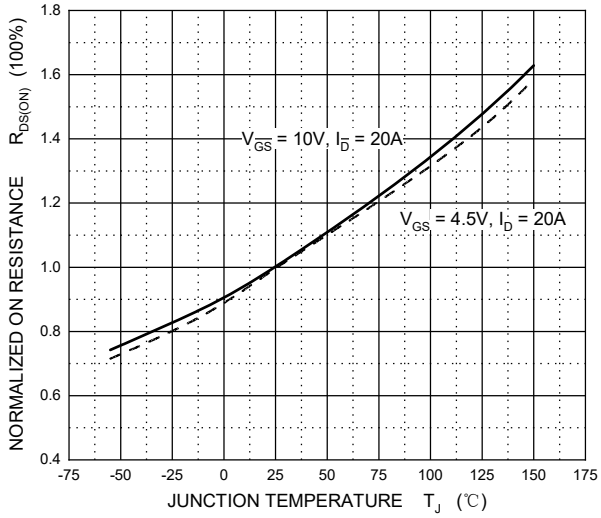
On Resistance vs. Drain Current



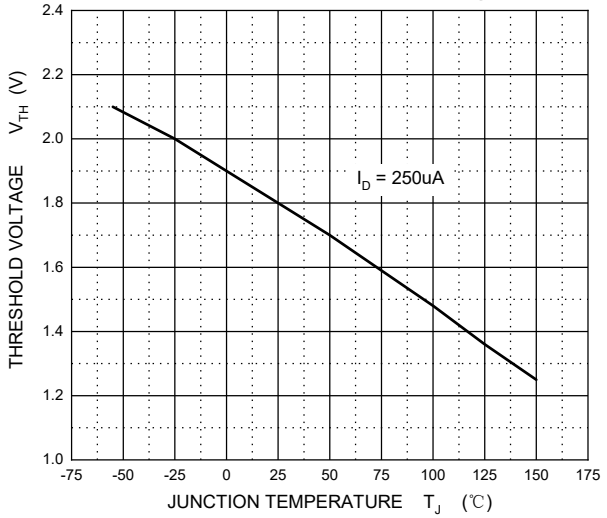
On Resistance vs. Gate-Source Bias



On Resistance vs. Junction Temperature

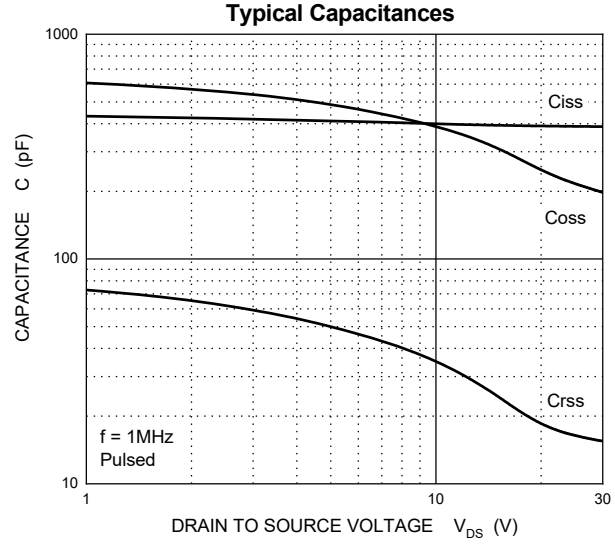


Gate-Source Threshold Voltage

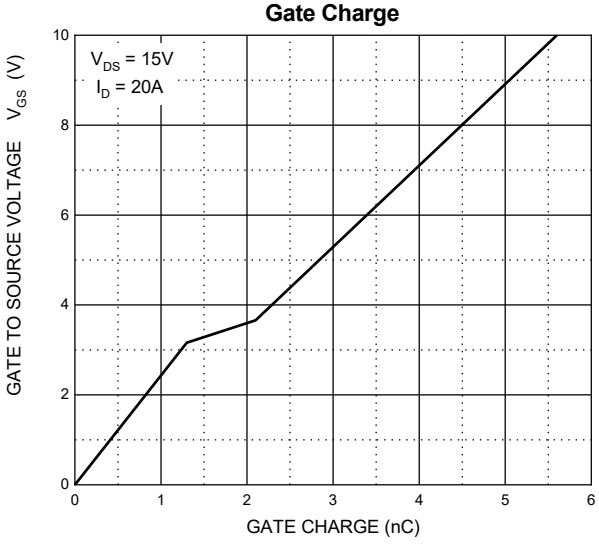


Typical Characteristics

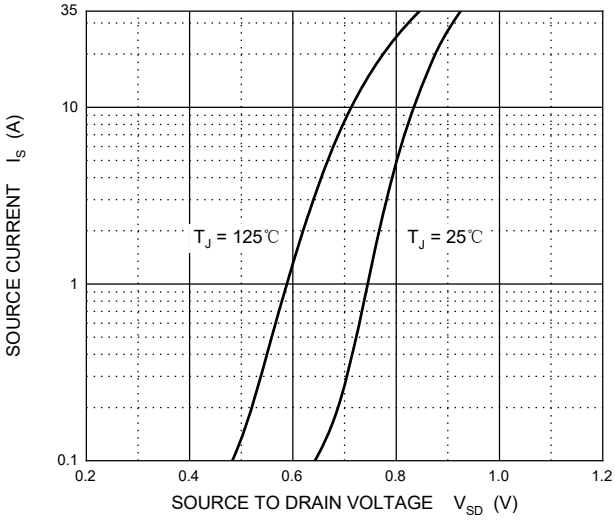
Typical Capacitances



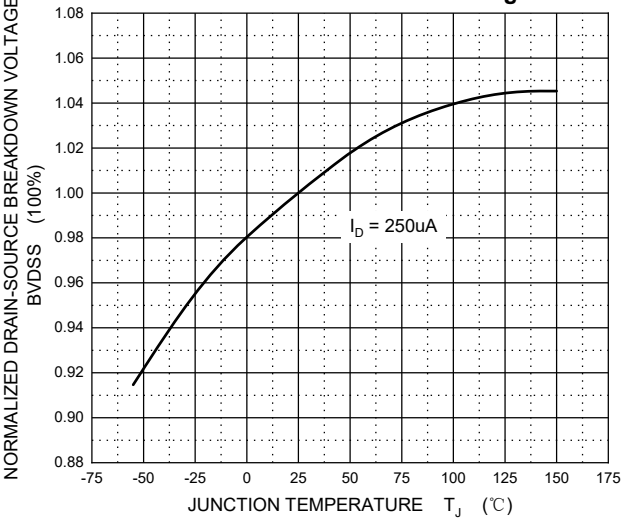
Gate Charge



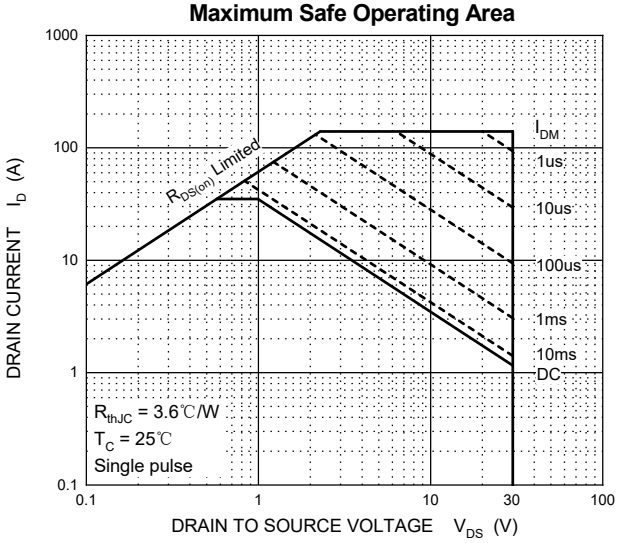
Source-Drain Diode Forward Characteristics



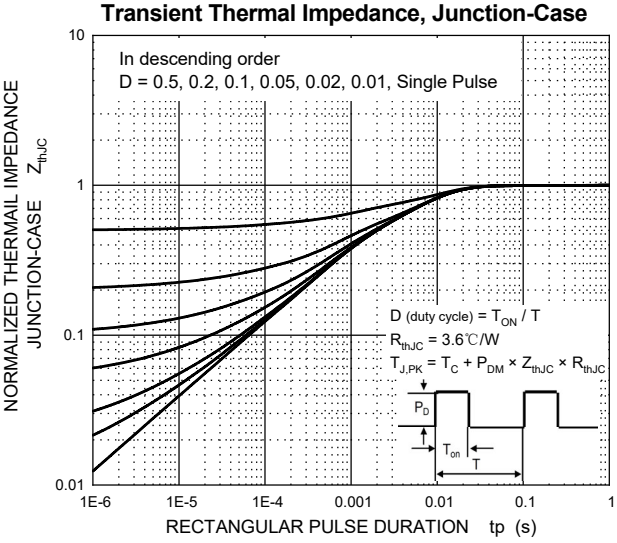
Drain-Source Breakdown Voltage



Maximum Safe Operating Area

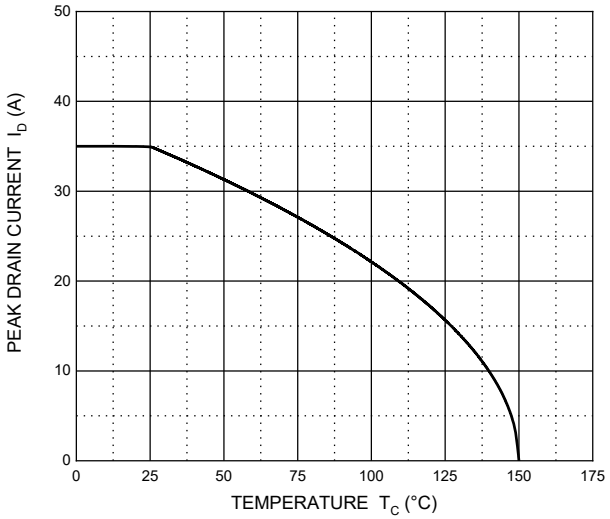


Transient Thermal Impedance, Junction-Case

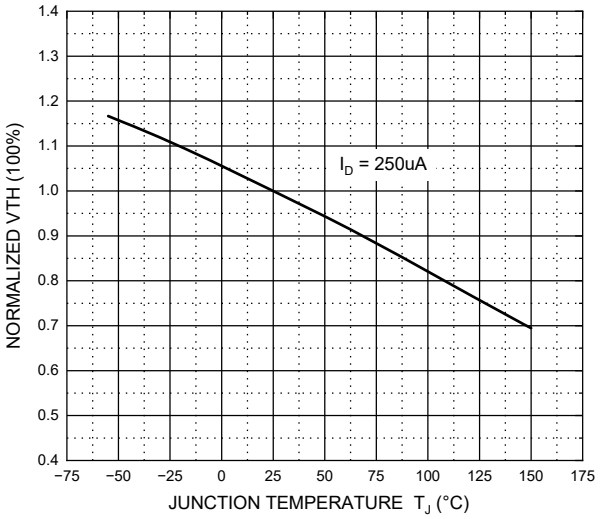


Typical Characteristics

ID vs. TC

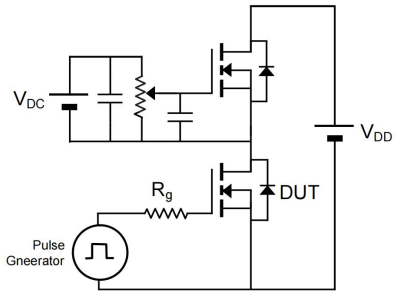


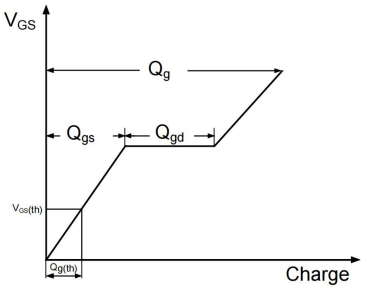
Normalized Threshold Voltage vs. Junction Temperature



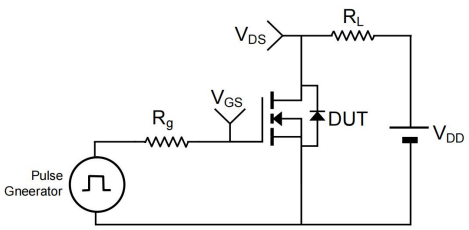
Test Circuit And Waveforms

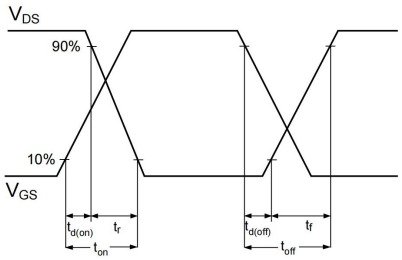
Gate Charge



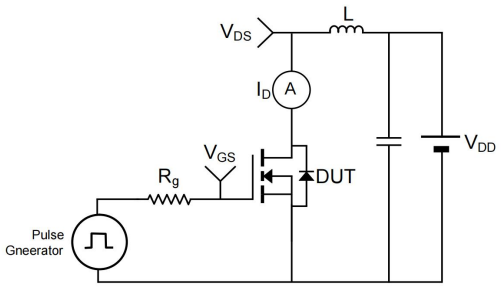


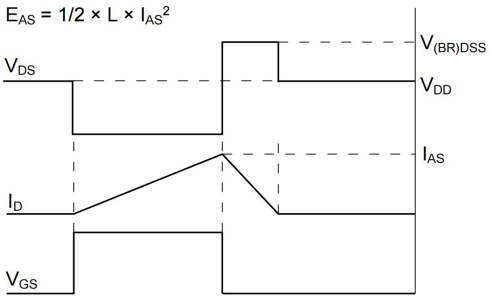
Resistive Load Switching Time



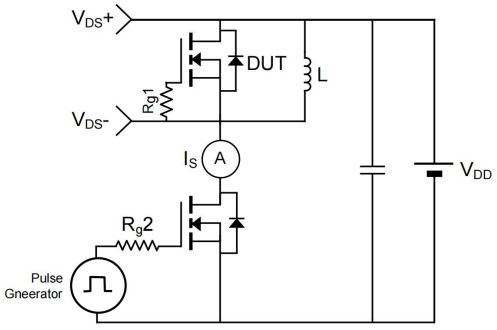


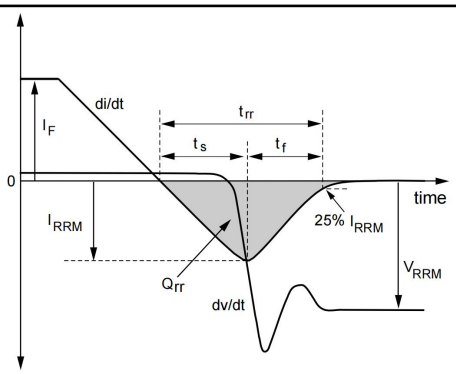
Un-clamped Inductive Load Switching



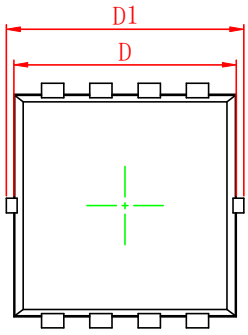


Drain-Source Body Diode Reverse Recovery

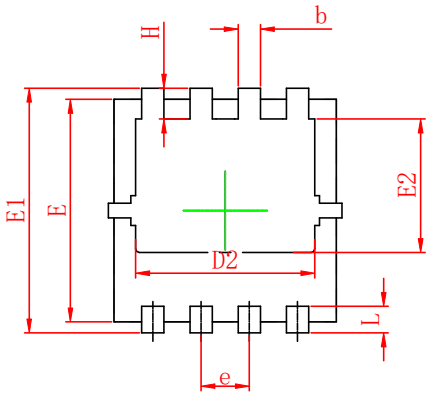




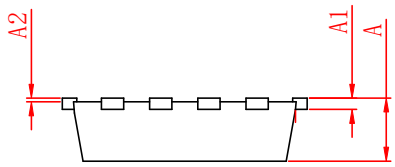
PDFNWB3.3×3.3-8L PACKAGE OUTLINE DIMENSIONS



Top View
[顶视图]



Bottom View
[背视图]

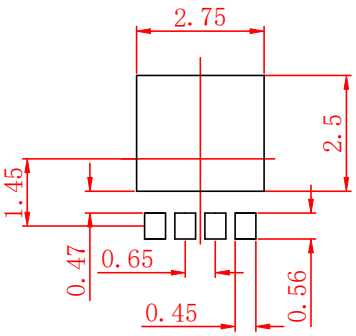


Side View
[侧视图]

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.65	0.90	0.026	0.035
A1	0.15 REF.		0.006 REF.	
A2	0~0.05		0~0.002	
D	2.90	3.20	0.114	0.126
D1	3.00	3.40	0.118	0.134
D2	2.30	2.70	0.091	0.106
E	2.90	3.20	0.114	0.126
E1	3.00	3.40	0.118	0.134
E2	1.60	1.90	0.063	0.075
b	0.20	0.42	0.008	0.017
e	0.65 BSC.		0.026 BSC.	
L	0.20	0.50	0.008	0.020
H	0.32	0.52	0.012	0.020

Notes：
1 Dimensions exclusive of mold gate burrs.
2 Dimensions exclusive of mold flash and cutting burrs.

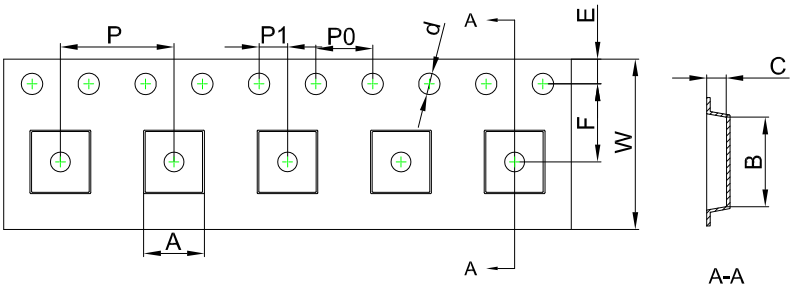
PDFNWB3.3×3.3-8L Suggested Pad Layout



Notes：
1 Controlling dimension：in millimeters.
2 General tolerance:±0.05mm.
2 The pad layout is for referance purpose only.

NOTICE
SZCJ reservesthe right to make modifications,enhancements,improvements,corrections or other changes without further notice to any product herein.SZCJ does not assume any liability arising out of the application or use of any product described herein.

PDFNWB3.3×3.3-8L Embossed Carrier Tape

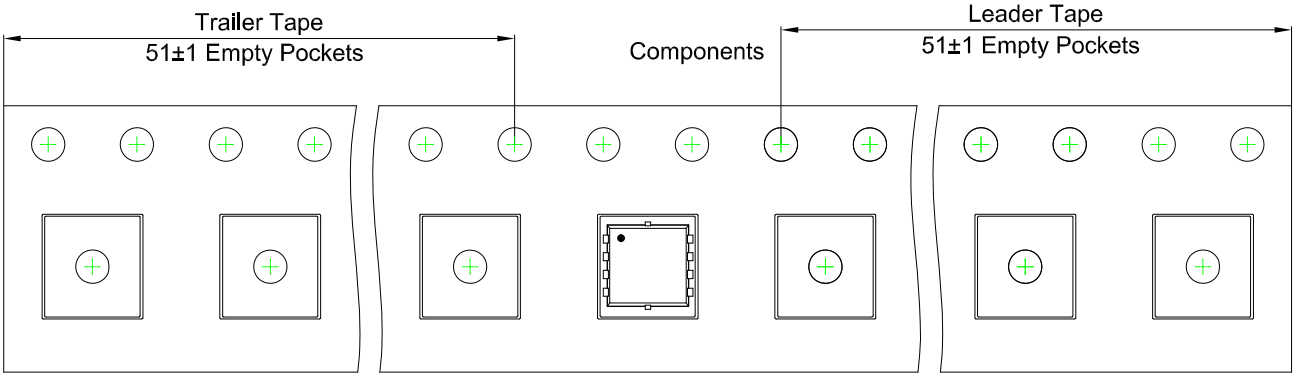


Packaging Description:

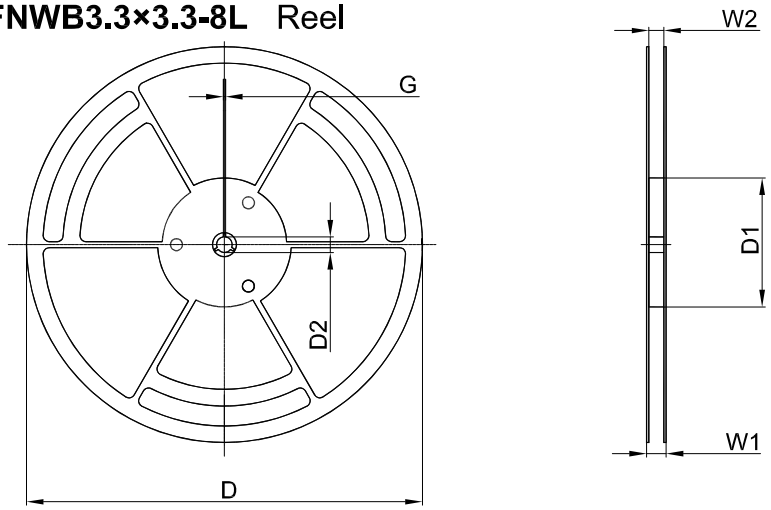
PDFNWB3.3x3.3-8L parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 5,000 units per 13" or 33.0 cm diameter reel. The reels are clear in color and is made of polystyrene plastic (anti-static coated).

Dimensions are in millimeter										
Pkg type	A	B	C	d	E	F	P0	P	P1	W
PDFNWB3.3×3.3-8L	3.55	3.55	1.10	Ø1.50	1.75	5.50	4.00	8.00	2.00	12.00

PDFNWB3.3×3.3-8L Tape Leader and Trailer



PDFNWB3.3×3.3-8L Reel



Dimensions are in millimeter						
Reel Option	**D**	**D1**	**D2**	**G**	**W1**	**W2**
13" Dia	φ330.00	100.00	13.00	1.90	17.60	12.40

Reel	Reel Size	Box	Box Size (mm)	Carton	Carton Size (mm)
5,000 pcs	13 inch	10,000 pcs	360×360×65	50,000 pcs	378×358×382